


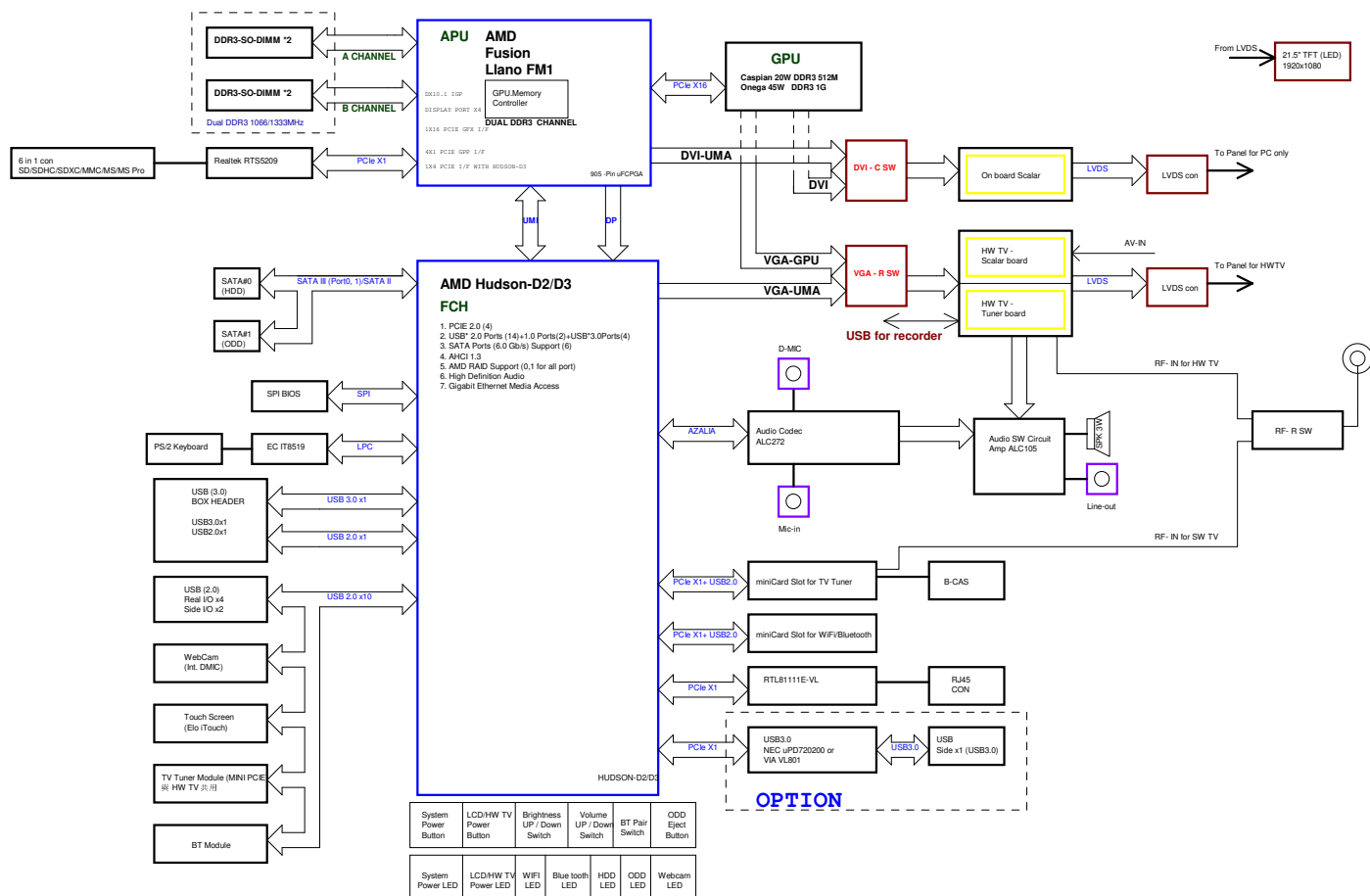
Lenovo B325 Schematics

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	T & I		OEM MODEL		Lenovo B325	Size	A3
	線路圖編號	715G4898MOC0000060	T&I MODEL	B325		Rev	0.3
	Key Component	COVER SHEET	PCB NAME	6150A0007903		備註	<REV>
	Date	Thursday, July 07, 2011	Sheet	1 of 53			



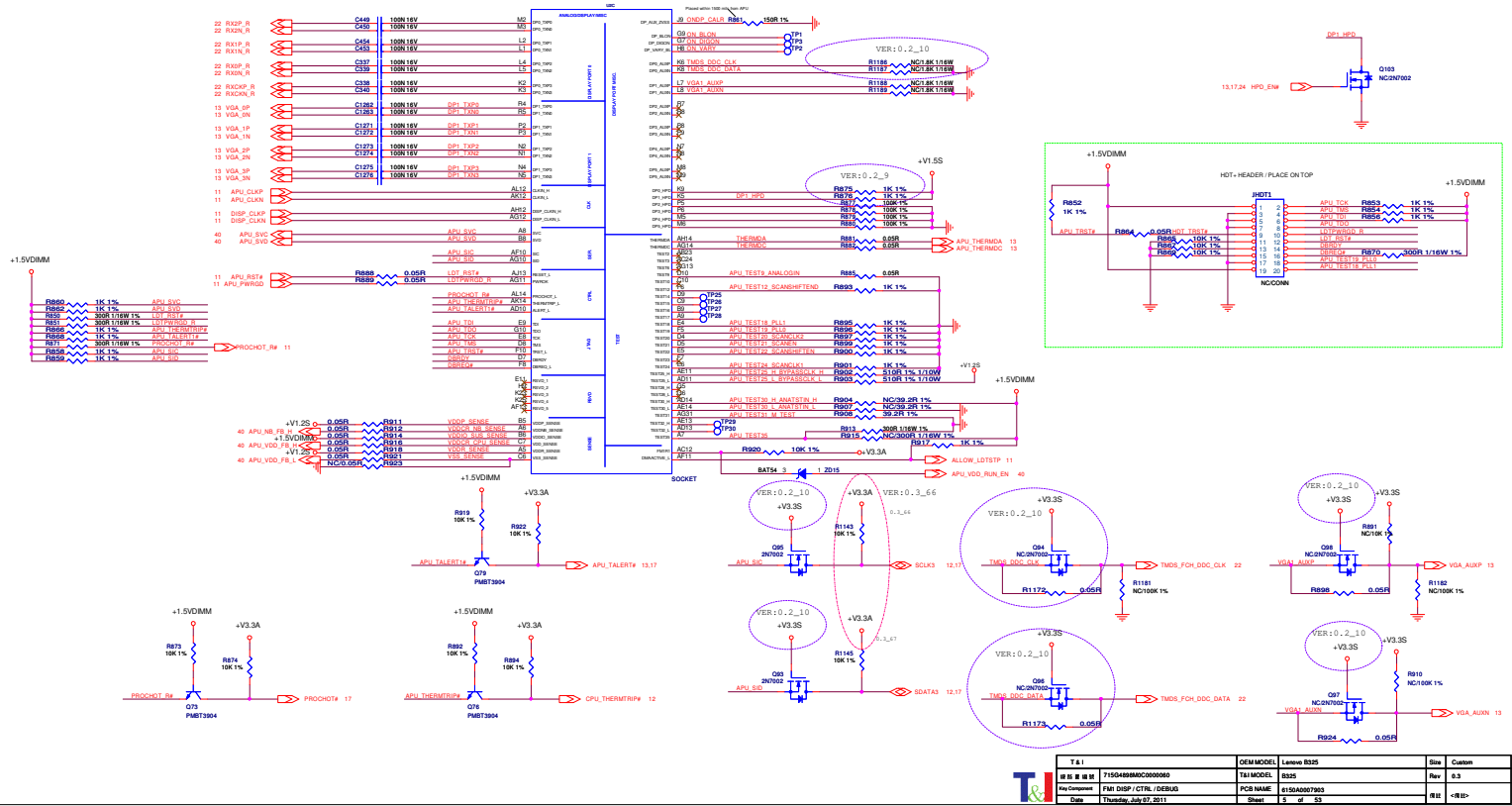
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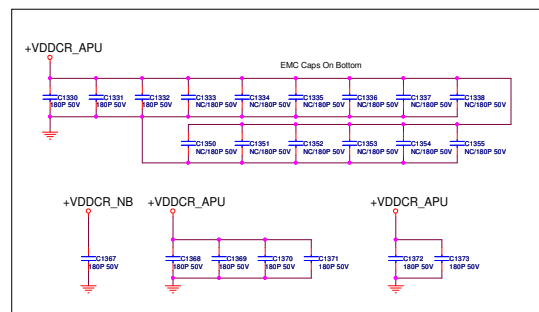
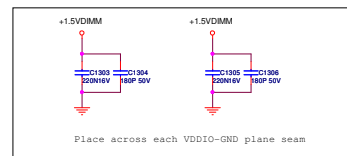
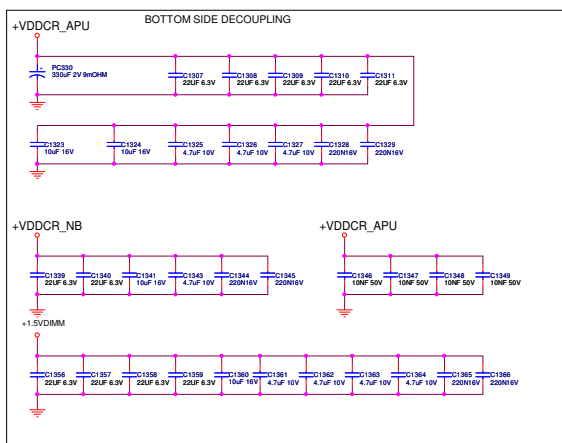
T & I	71904881MCC0000000	DEM MODEL	Lanovo 8925	Size	C
零件圖號	71904881MCC0000000	TAM MODEL	8925	Rev	0.3
Rev/Component	BLOCK DIAGRAM	PCB NAME	8150A0007913	備註	<WEB>
Date	Rev:05 1/25/2011	Sheet	2	of	13



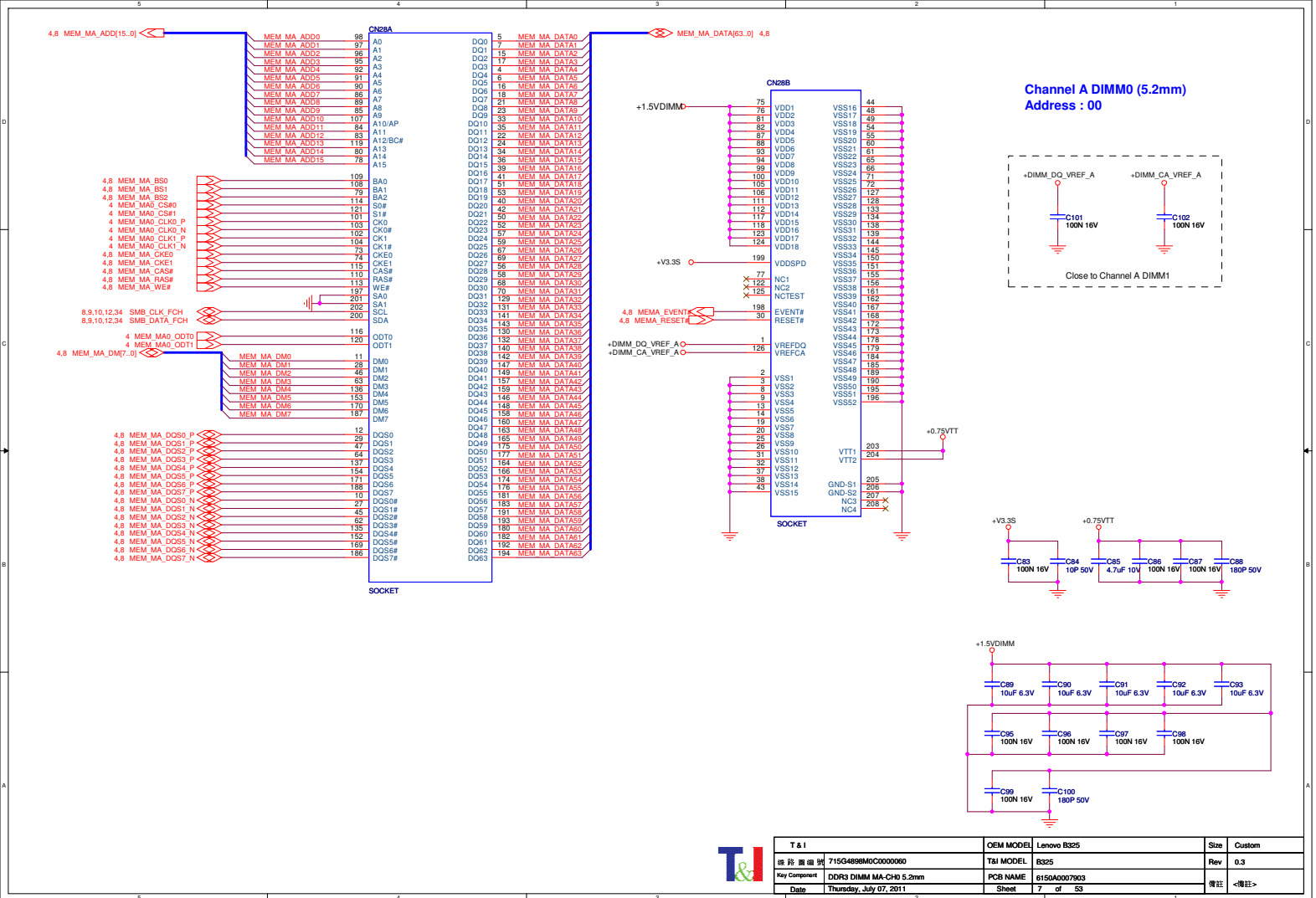
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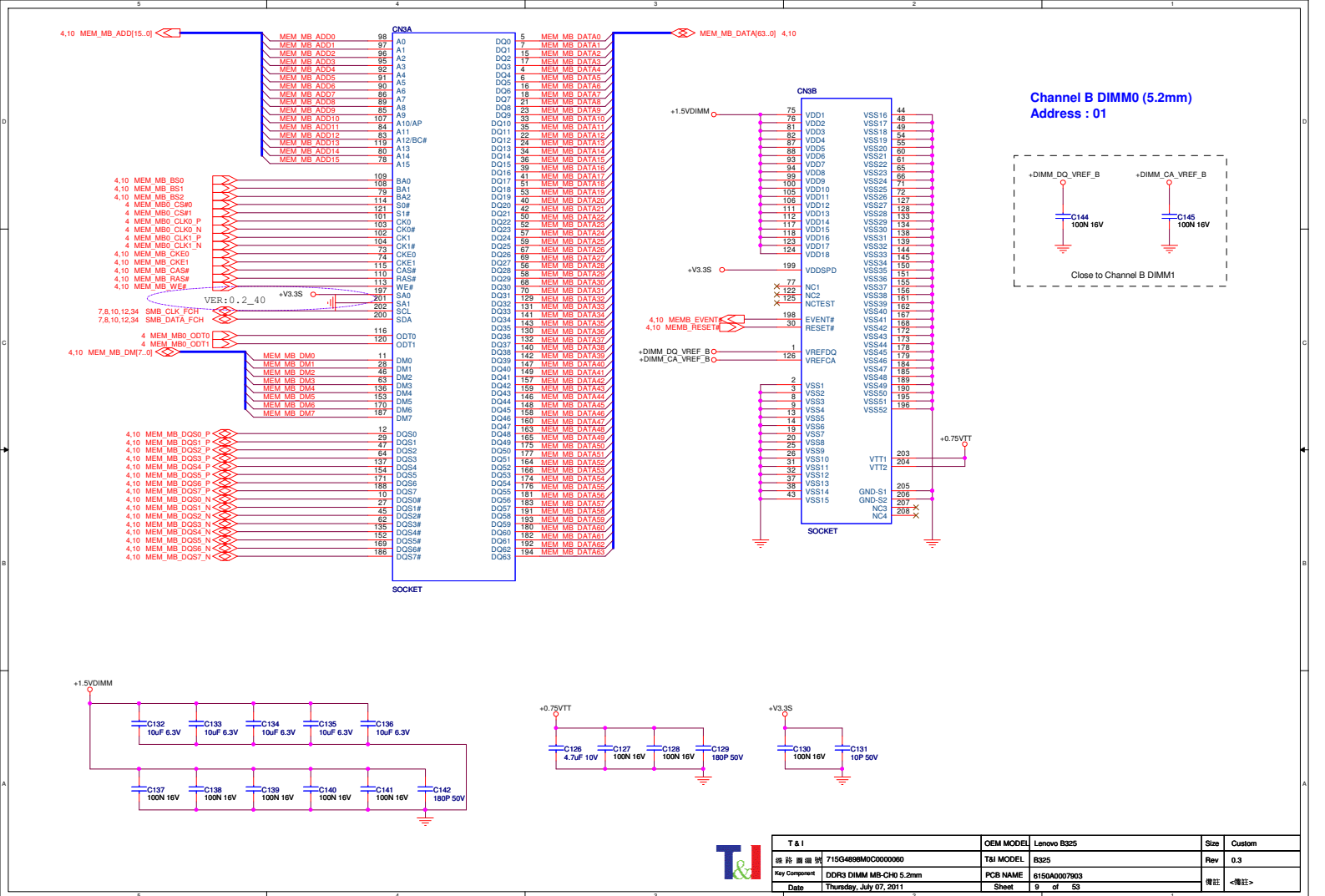




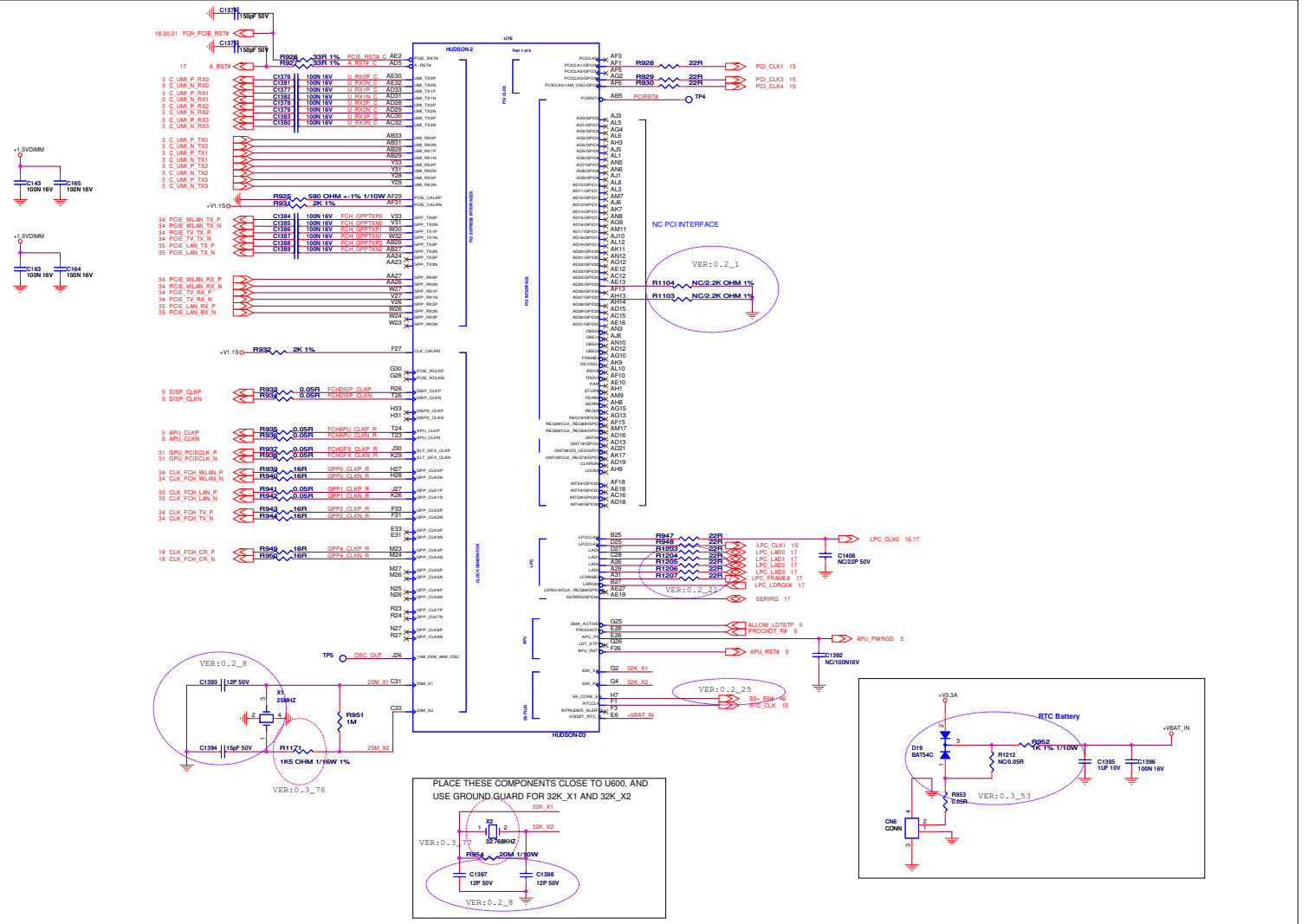
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機殼圖號	715G4899A0C0000060	T&I MODEL	B325	Rev	0.3
Key/Component	Fm1 PWR / GND	PCB NAME	6150A0007903	備註	<備註>
Date	Thursday, Jul 07, 2011	Sheet	6 of 53		



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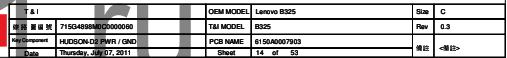


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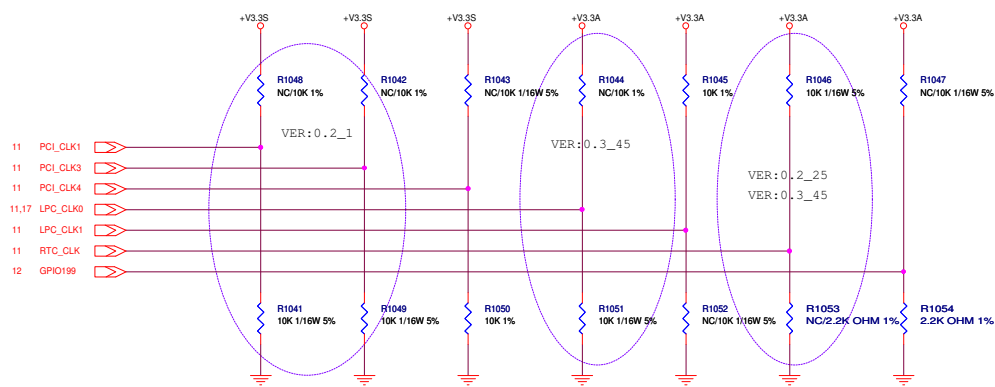
TS1	TS1	TS1	TS1	TS1
TS1	TS1	TS1	TS1	TS1
TS1	TS1	TS1	TS1	TS1
TS1	TS1	TS1	TS1	TS1
TS1	TS1	TS1	TS1	TS1



關注



FCH REQUIRED STRAPS



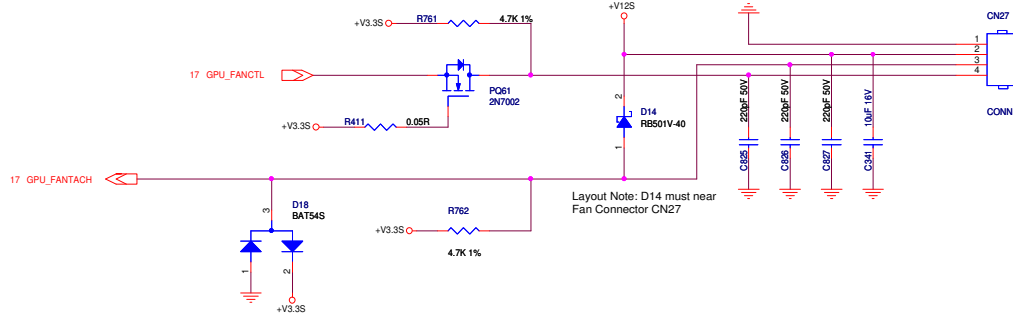
	PCI_CLK1	PCI_CLK3	PCI_CLK4	LPC_CLK0	LPC_CLK1	RTC_CLK	GPIO199
PULL HIGH	ALLOW PCIE GEN2	USE DEBUG STRAPS	NON-FUSION APU CLOCK MODE	EC ENABLED	CLKGEN ENABLED	S5+ DISABLE	ROM TYPE: H = LPC ROM
PULL LOW	FORCE PCIE GEN1	IGNORE DEBUG STRAPS	FUSION APU CLOCK MODE	EC DISABLED	CLKGEN DISABLED	S5+ ENABLE	L = SPI ROM



T & I	OEM MODEL	Lenovo B325	Size	B
版 本 号	715C4898MOC0000060	T&I MODEL	B325	Rev
Key Component	HUDSON-D2 STRAP / DEBUG	PCB NAME	6150A0007903	備 註
Date	Thursday, July 07, 2011	Sheet	15 of 53	<備註>

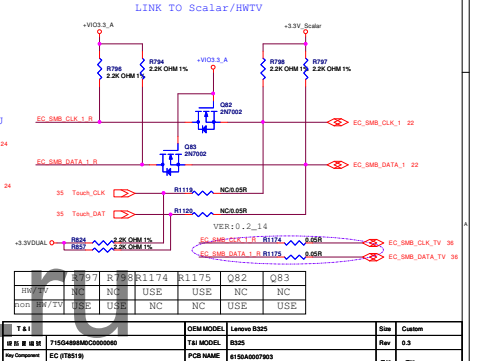
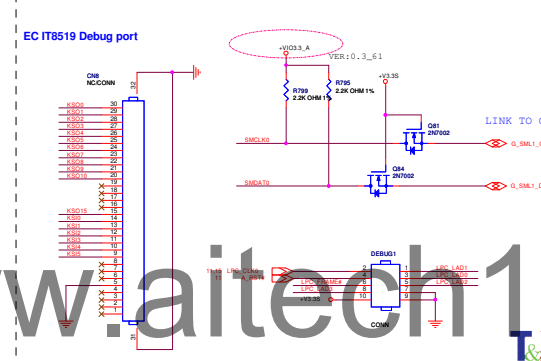
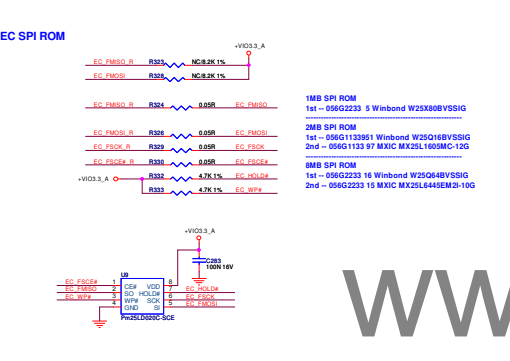
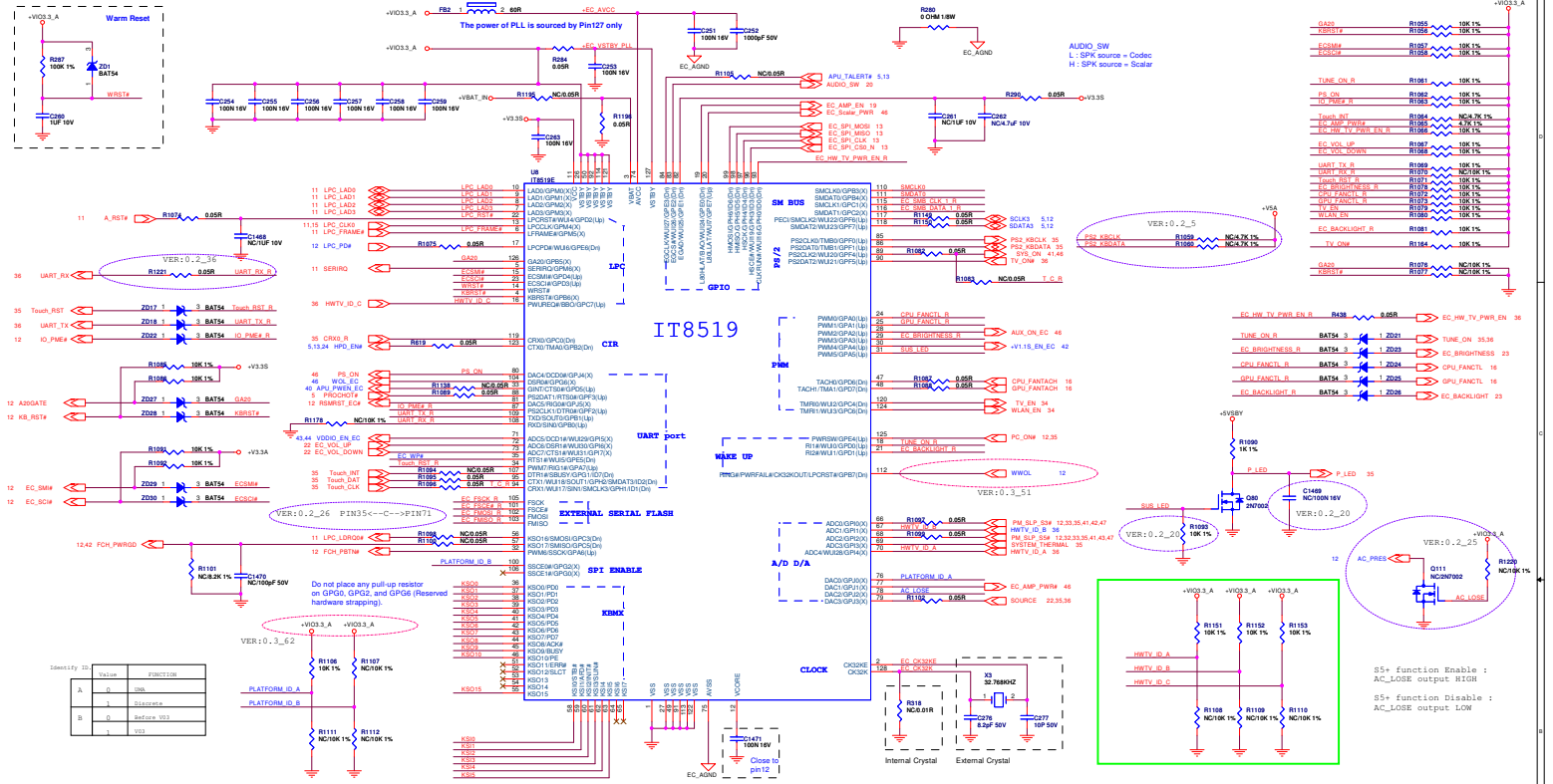
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GPU FAN



T & I		OEM MODEL	Lenovo B325	Size	B
條 碼 圖 編 號	715G4898MOC0000060	T&I MODEL	B325	Rev	0.3
Key Component	FAN	PCB NAME	6150A0007903		
Date	Thursday, July 07, 2011	Sheet	16 of 53	備註	<備註>

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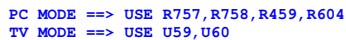
T & I	EC IT8519	DEM MODEL	Lenovo B305	Size	Custom
PCB NAME	EC IT8519	PCB NAME	6200A0007000	Rev	0.3
DATE	Thursday, July 07, 2011	Sheet	17 of 50	FILE	



注册	注册
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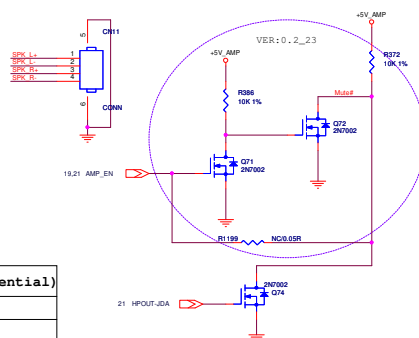
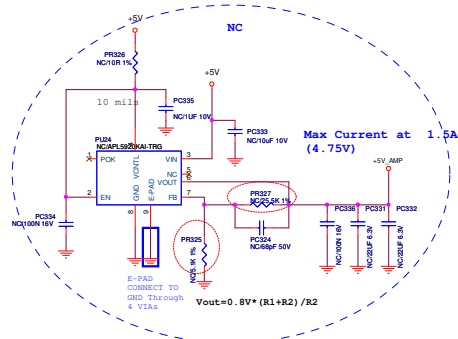
IN1=0 ; NC1 => COM1, IN2=0 ; NC2 => COM2
IN1=1 ; NO1 => COM1, IN2=1 ; NO2 => COM2

AUDIO_SW
L : SPK source = Codec
H : SPK source = Scalar



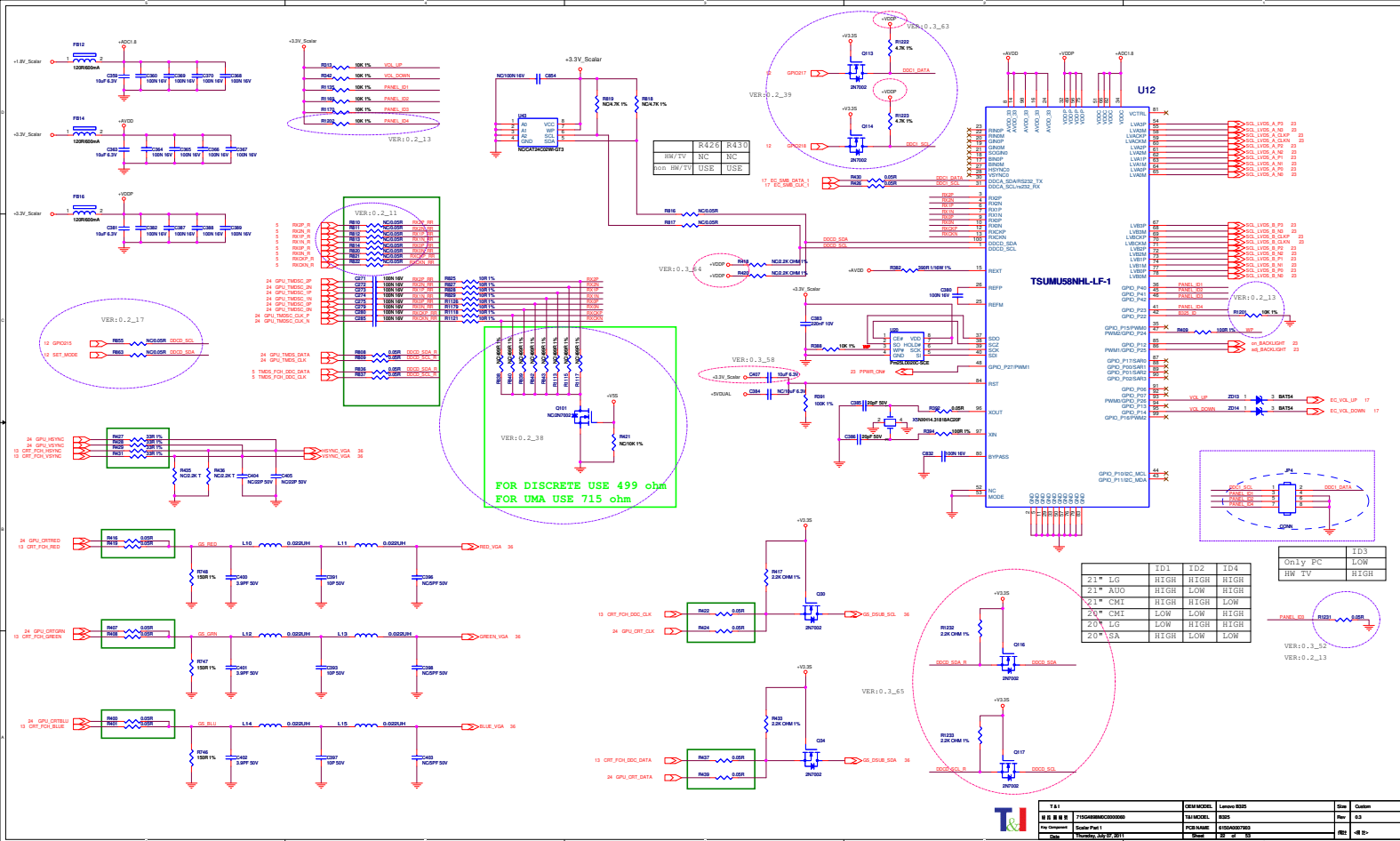
```
IN1=0 ; NC1 => COM1, IN2=0 ; NC2 => COM2
IN1=1 ; NO1 => COM1, IN2=1 ; NO2 => COM2

AUDIO_SW
L : SPK_source = Codec
H : SPK_source = Scalar
```



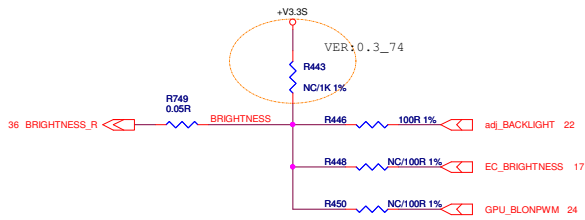
R1	R2	R3	R4	Gain(single_end)	Gain(Differential)
NC	NC	0	0	5dB	11dB
0	NC	NC	0	8dB	14dB
NC	0	0	NC	13dB	19dB
0	0	NC	NC	19dB	25dB

R1	R2	R3	R4	Gain(single_end)	Gain(Differential)
NC	NC	0	0	5dB	11dB
0	NC	NC	0	8dB	14dB
NC	0	0	NC	13dB	19dB
0	0	NC	NC	19dB	25dB

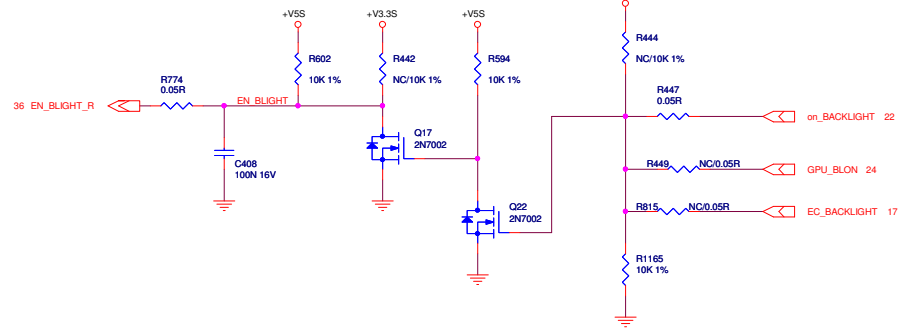



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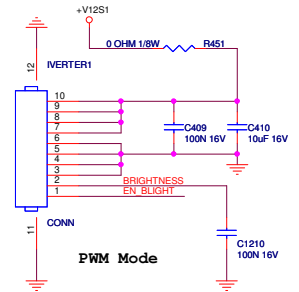
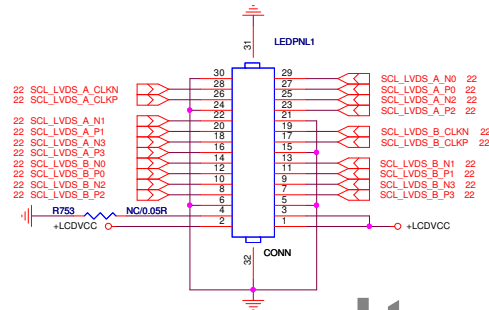
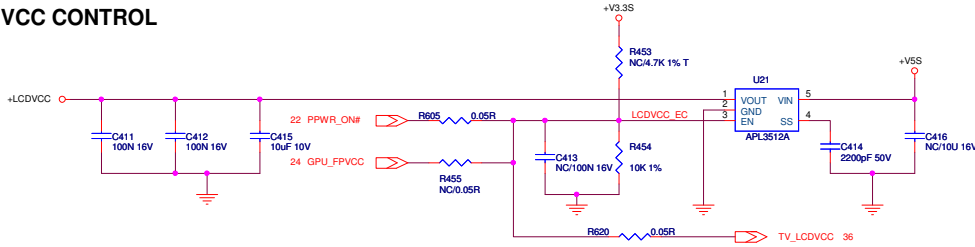
BRIGHTNESS CONTROL



BACKLIGHT ENABLE



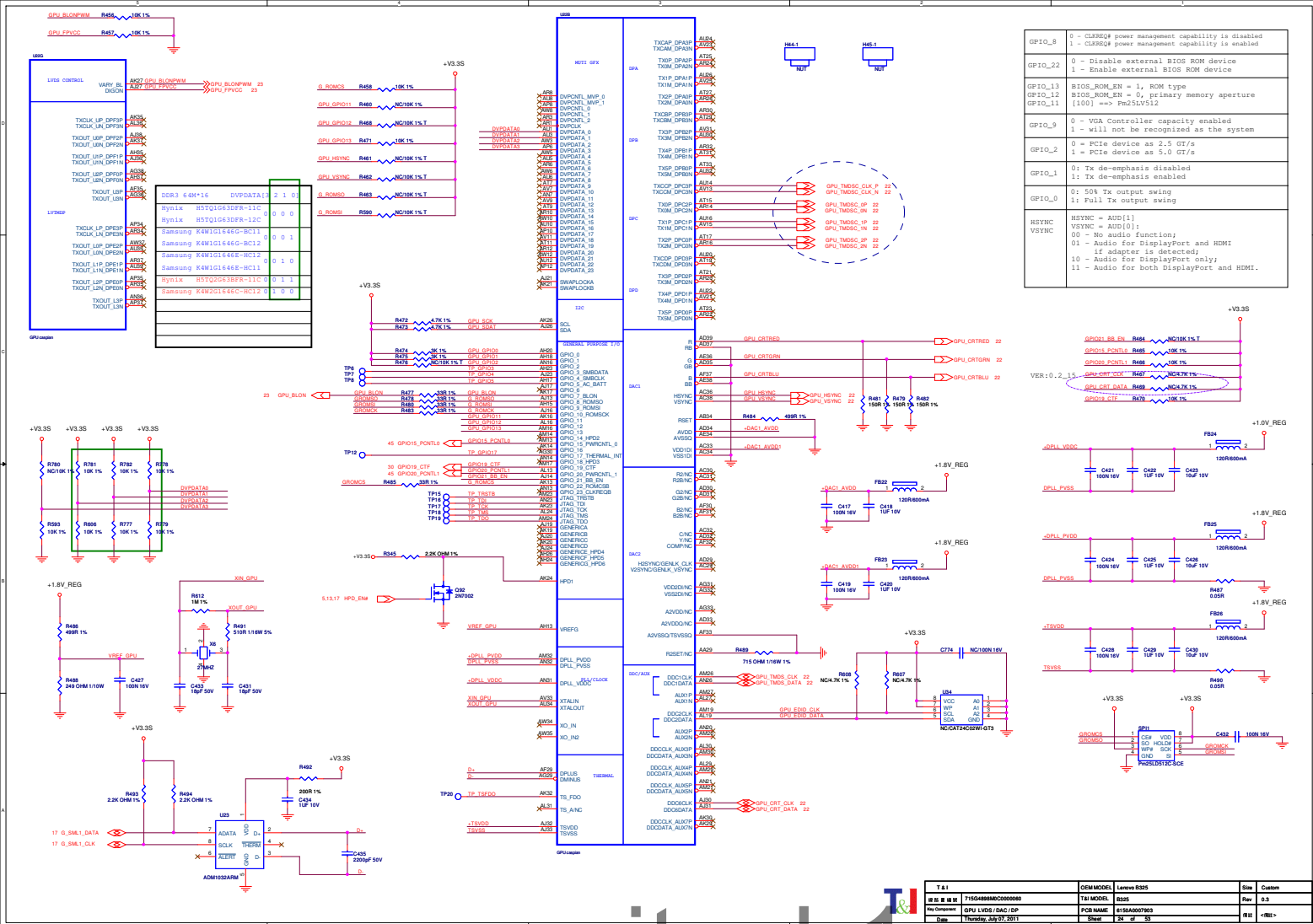
PANEL VCC CONTROL



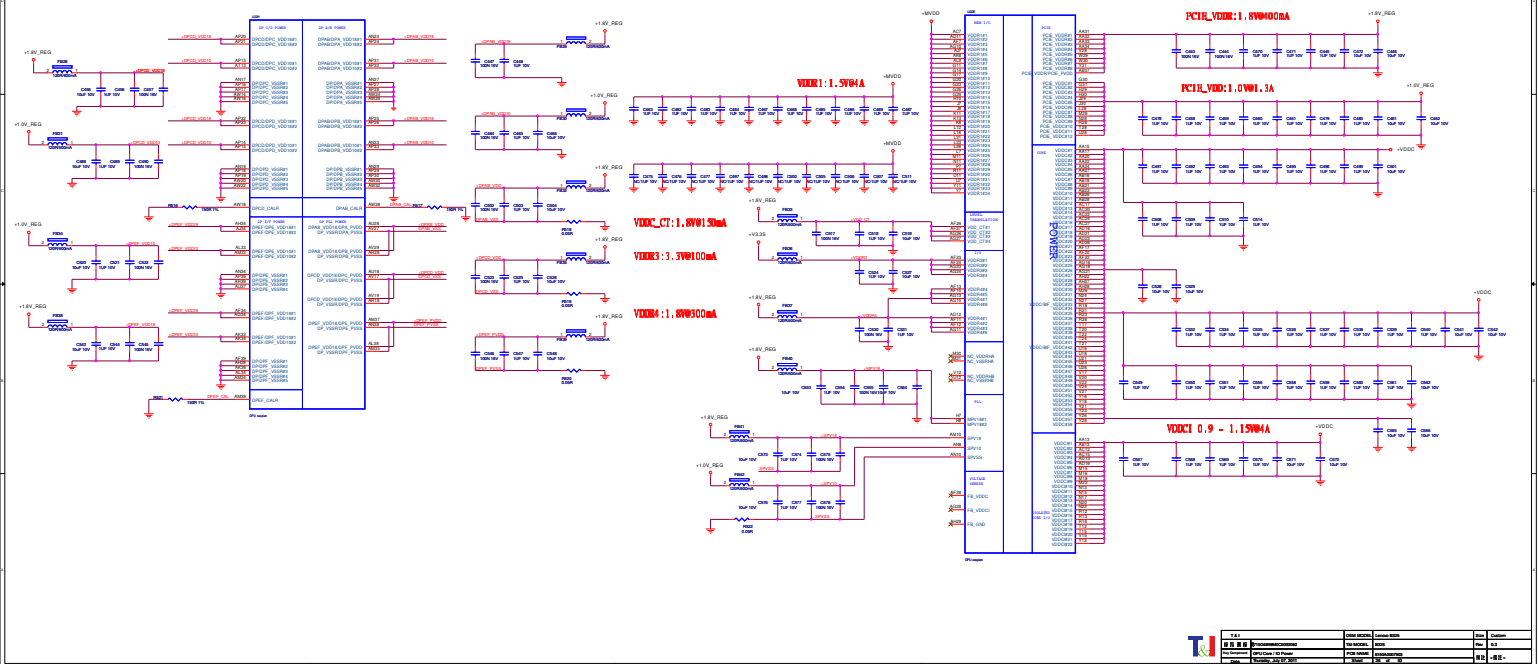
www.aitech1.ru



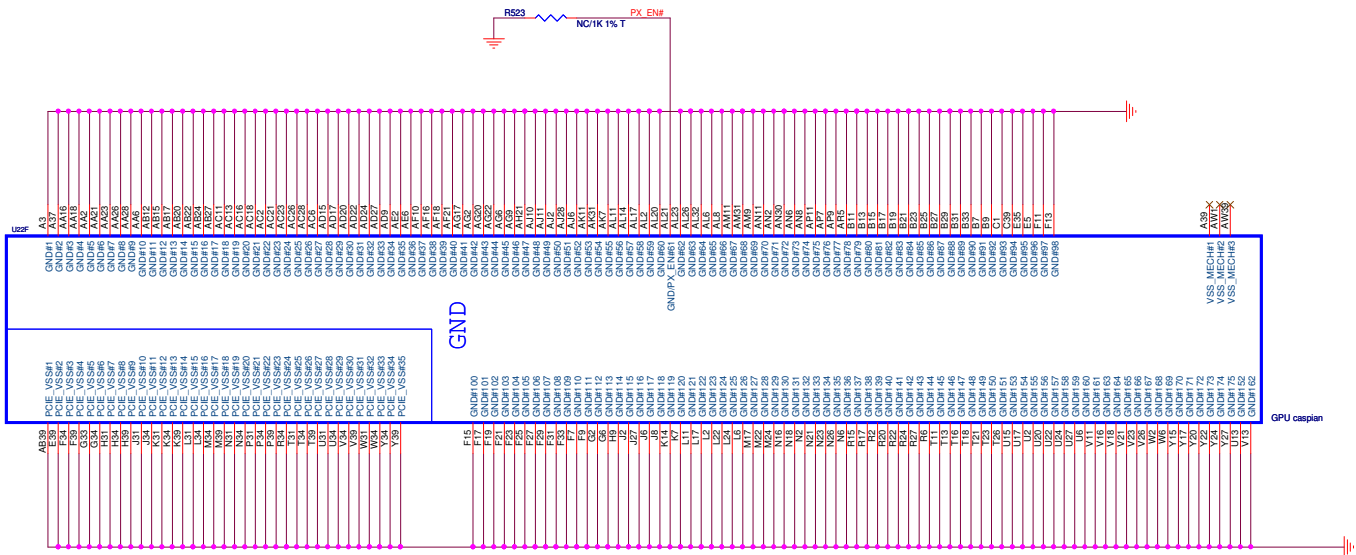
T & I	OEM MODEL	Lenovo B325	Size	Custom
715G4898MOC0000060	T&I MODEL	B325	Rev	0.3
Key Component	Scaler Part 2	PCB NAME	6150A0007903	備註
Date	Thursday, July 07, 2011	Sheet	23 of 53	<備註>



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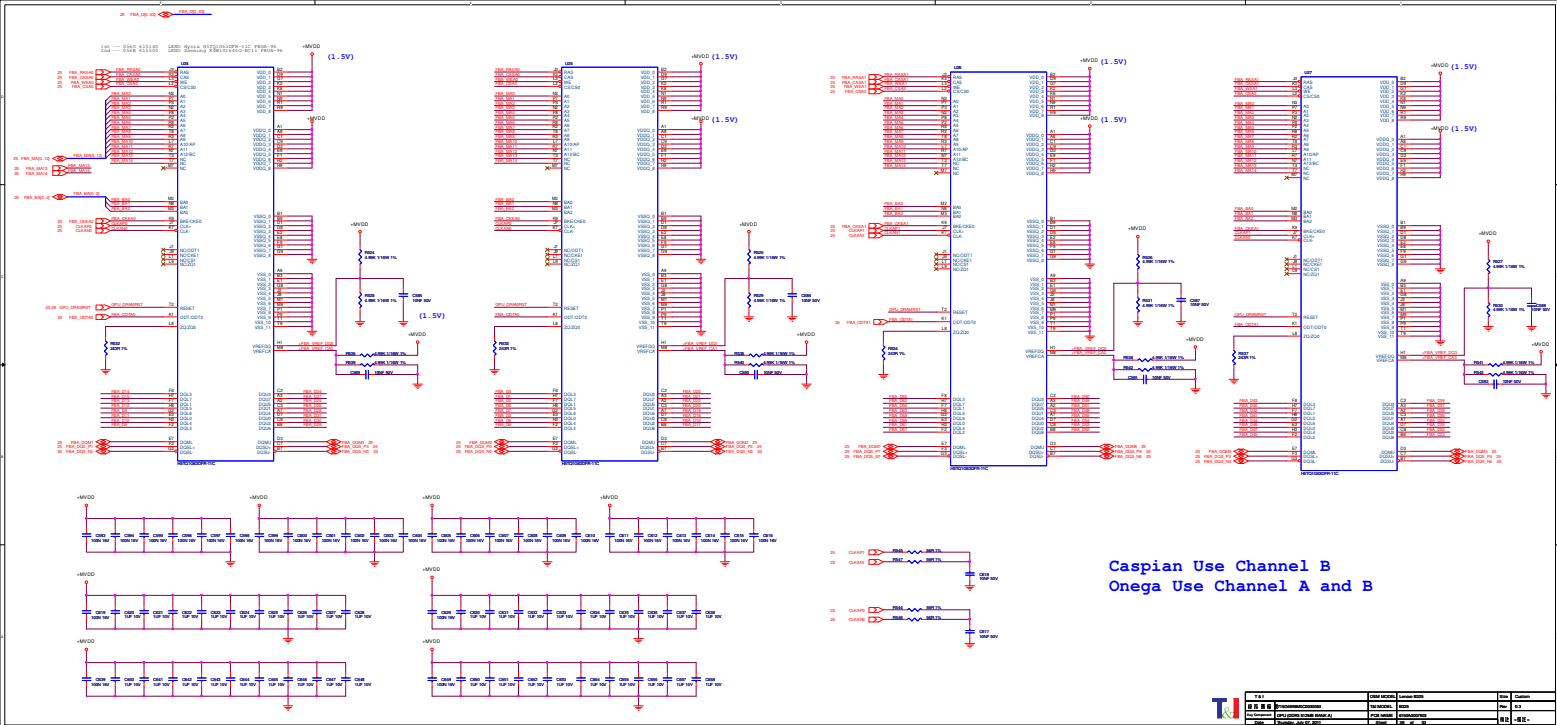


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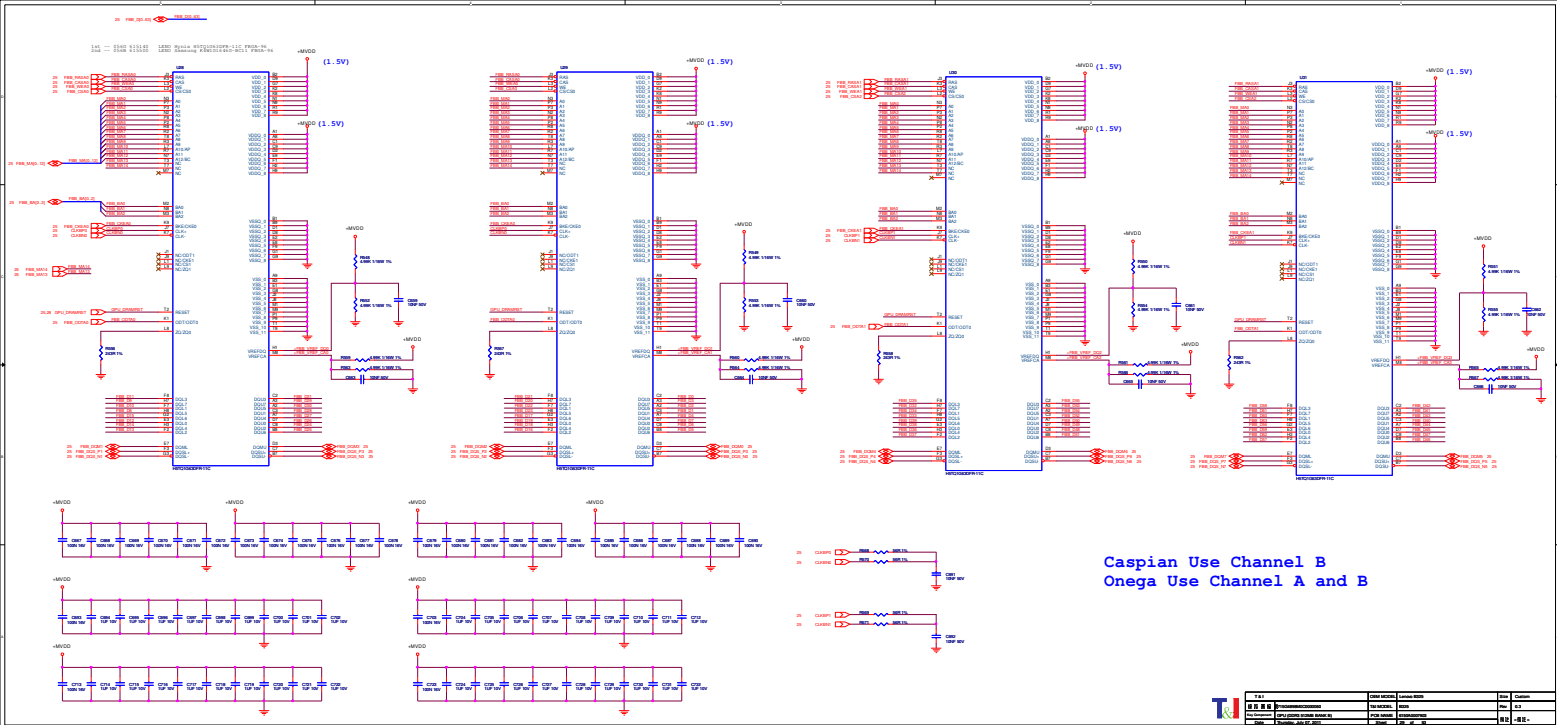


T & I		OEM MODEL	Lenovo B325	Size	B
統 路 圖 編 號	715G4898MOC0000060	T&I MODEL	B325	Rev	0.3
Key Component	GPU GND	PCB NAME	6150A0007903	備註 <備註>	
Date	Thursday, July 07, 2011	Sheet	27 of 53		

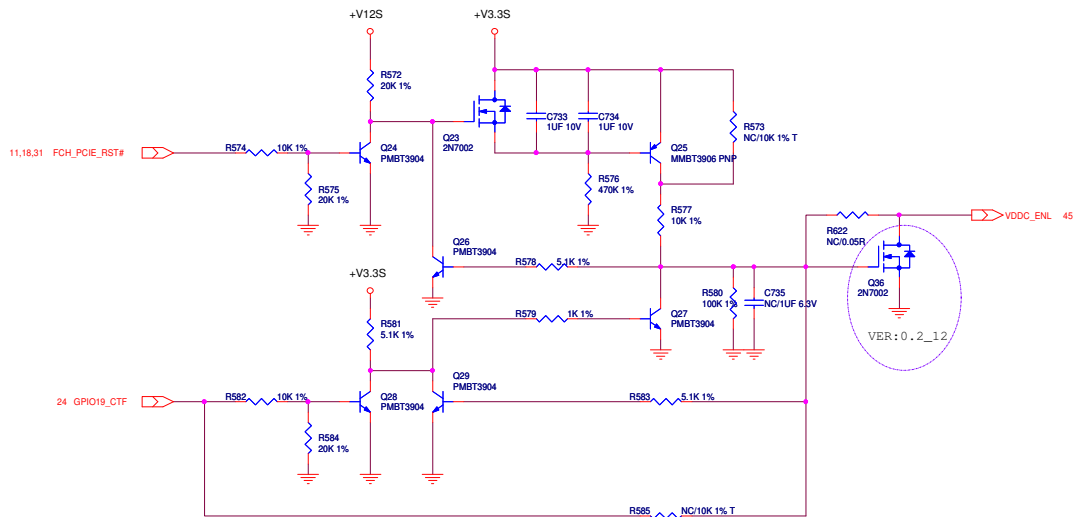
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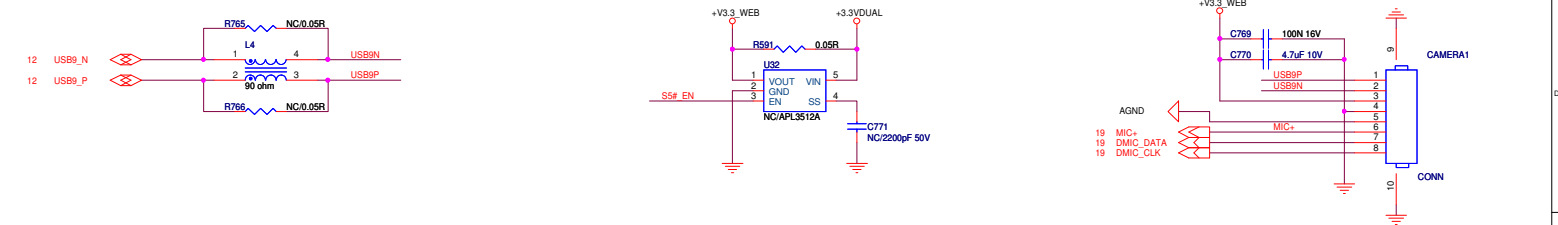
www.aitech1.ru



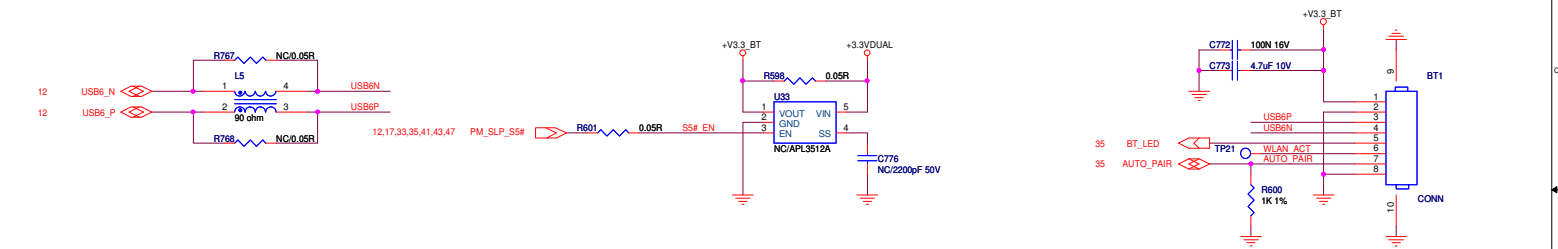
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統 路 圖 編 號	715G4898MOC0000060	T&I MODEL	B325	Rev
Key Component	GPU CTF	PCB NAME	6150A0007903	0.3
Date	Thursday, July 07, 2011	Sheet	30 of 53	備註 <備註>

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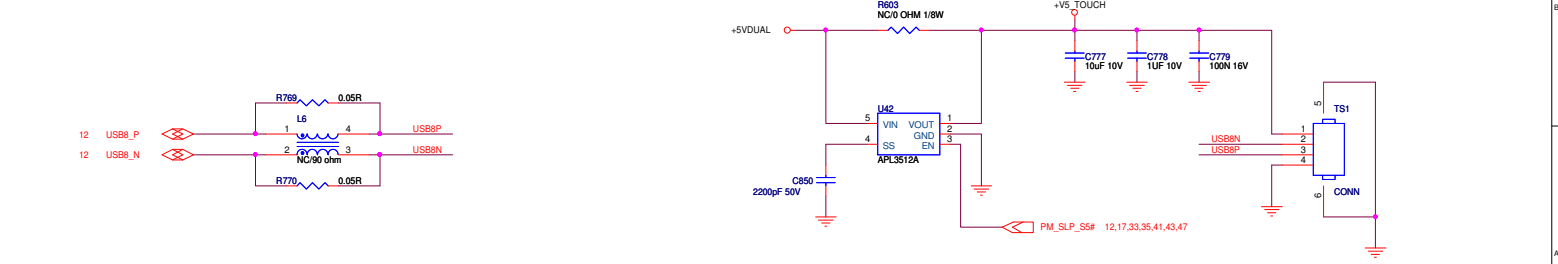
Camear + Mic



BT



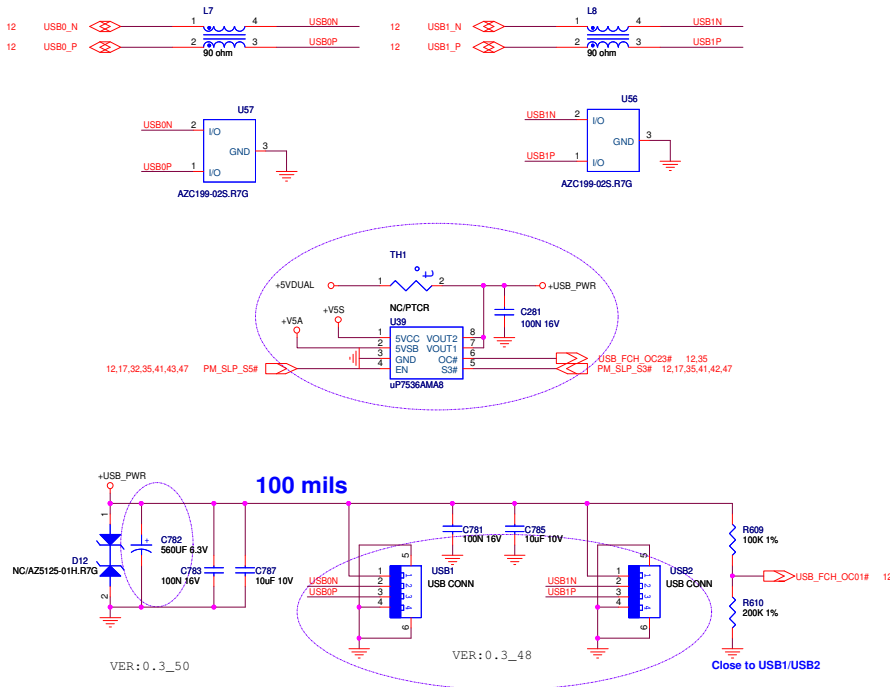
Touch Screen



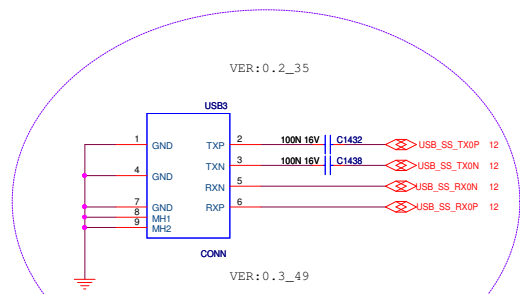
T & I	OEM MODEL	Lenovo B325	Size	B
統 路 圖 編 號	715G4898MOC0000060	T&I MODEL	B325	Rev
Key Component	Webcam, BT, Touch Pad	PCB NAME	6150A0007903	備註
Date	Thursday, July 07, 2011	Sheet	32 of 53	<備註>

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USB2.0



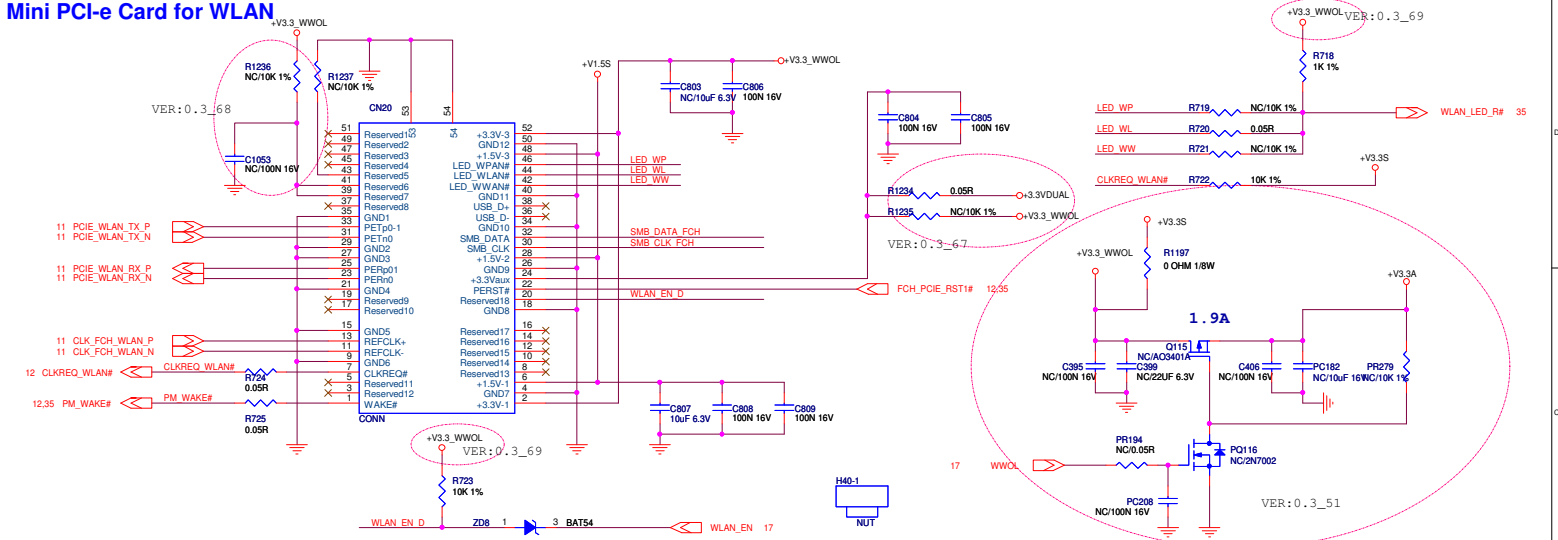
USB3.0



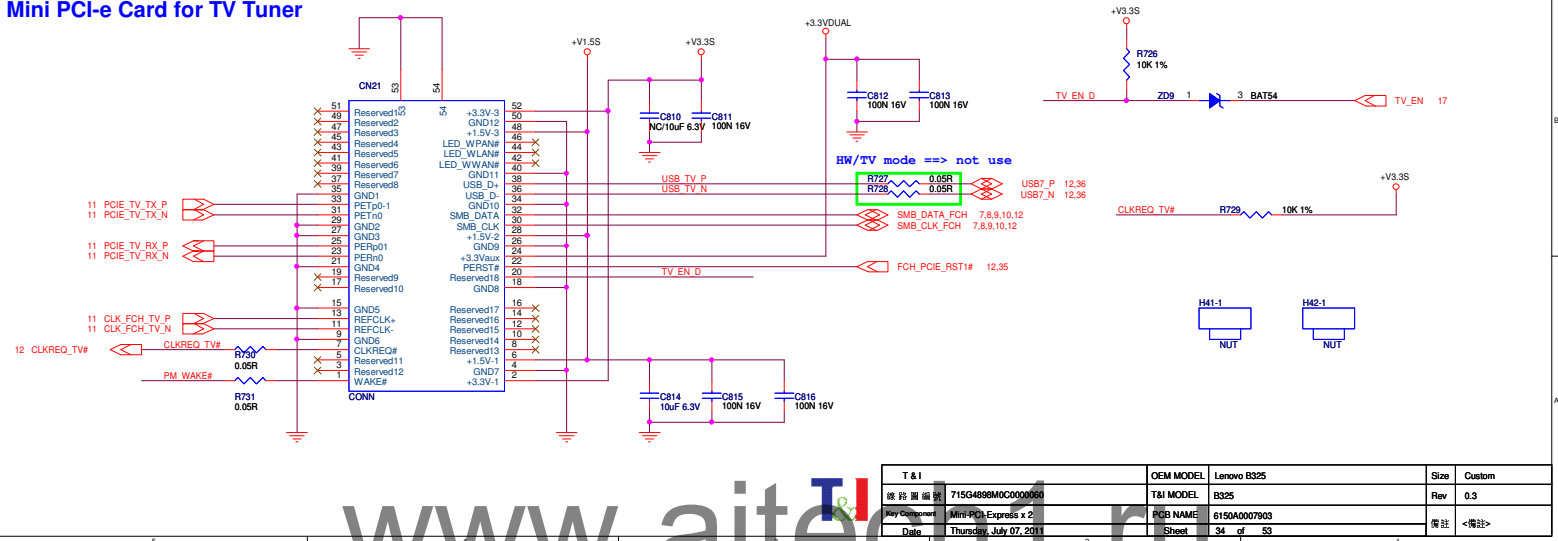
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統 路 圖 編 號	T&I MODEL	B325	Rev	0.3
Key Component	USB CONN	PCB NAME	6150A0007903	備註
Date	Thursday, July 07, 2011	Sheet	33 of 53	<備註>

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Mini PCI-e Card for WLAN

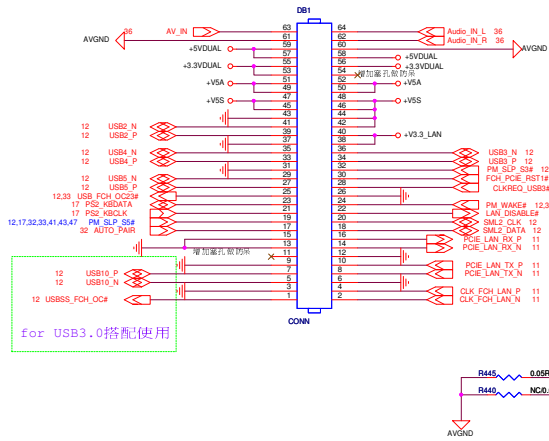


Mini PCI-e Card for TV Tuner

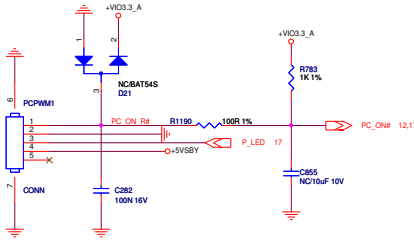


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Key Component	Mini-PCI-Express x 2	POB NAME	6150A0007903	備 註
Date	Thursday, July 07, 2011	Sheet	34 of 53	< 前 頁

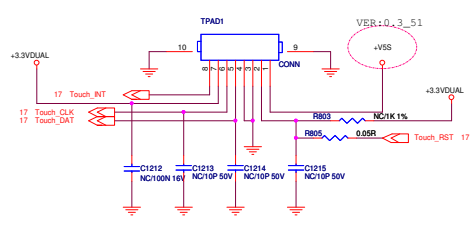
REAL IO CONN



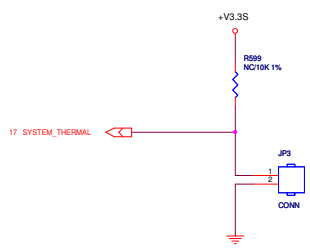
PC PWM CONN



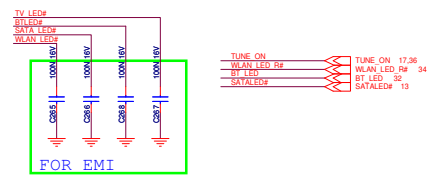
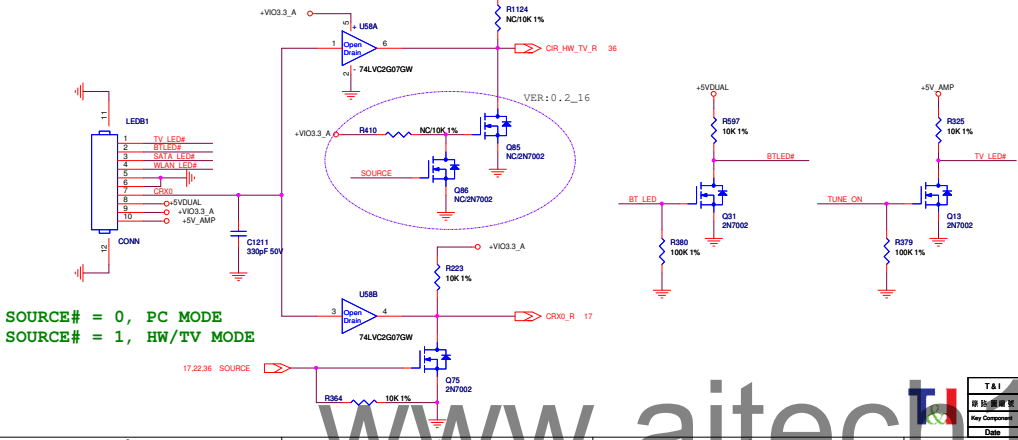
FUNCTION TOUCH PAD CONN



SYSTEM THERMAL CONN

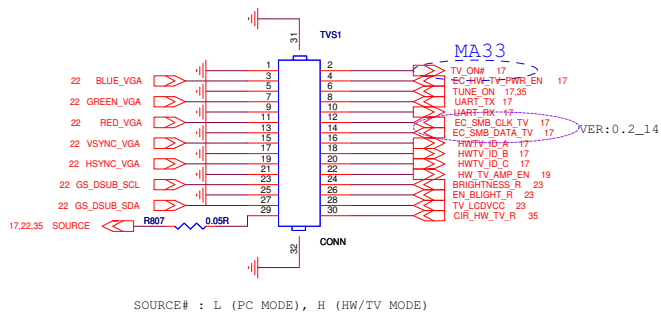


FUNCTION LED CONN

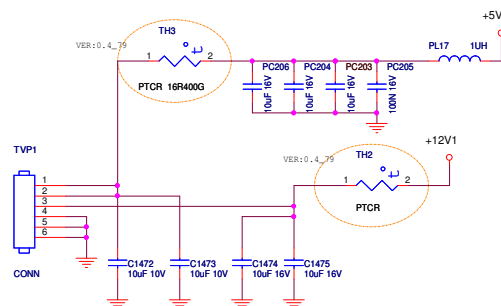
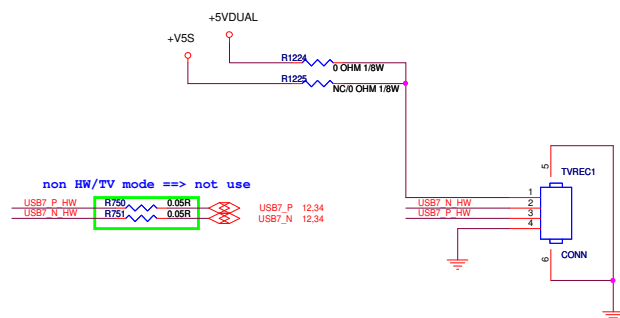
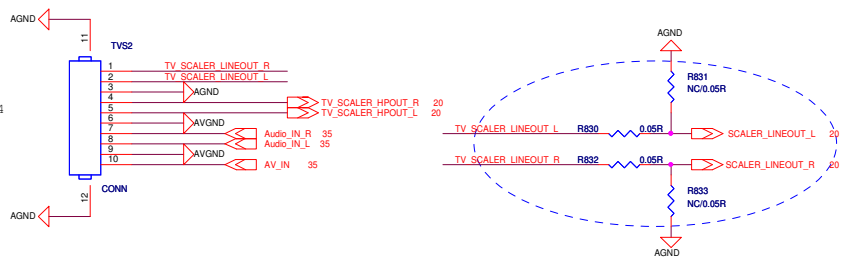


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T & I	OEM MODEL	Size	Custom
715G4898M0C0000000	Lenovo B325	B325	Rev 0.3
DB_PC_ON_TX_FL	PCB NAME	6150A0007903	Sheet 35 of 55
Thursday, July 07, 2017	Date		備註 <備註>

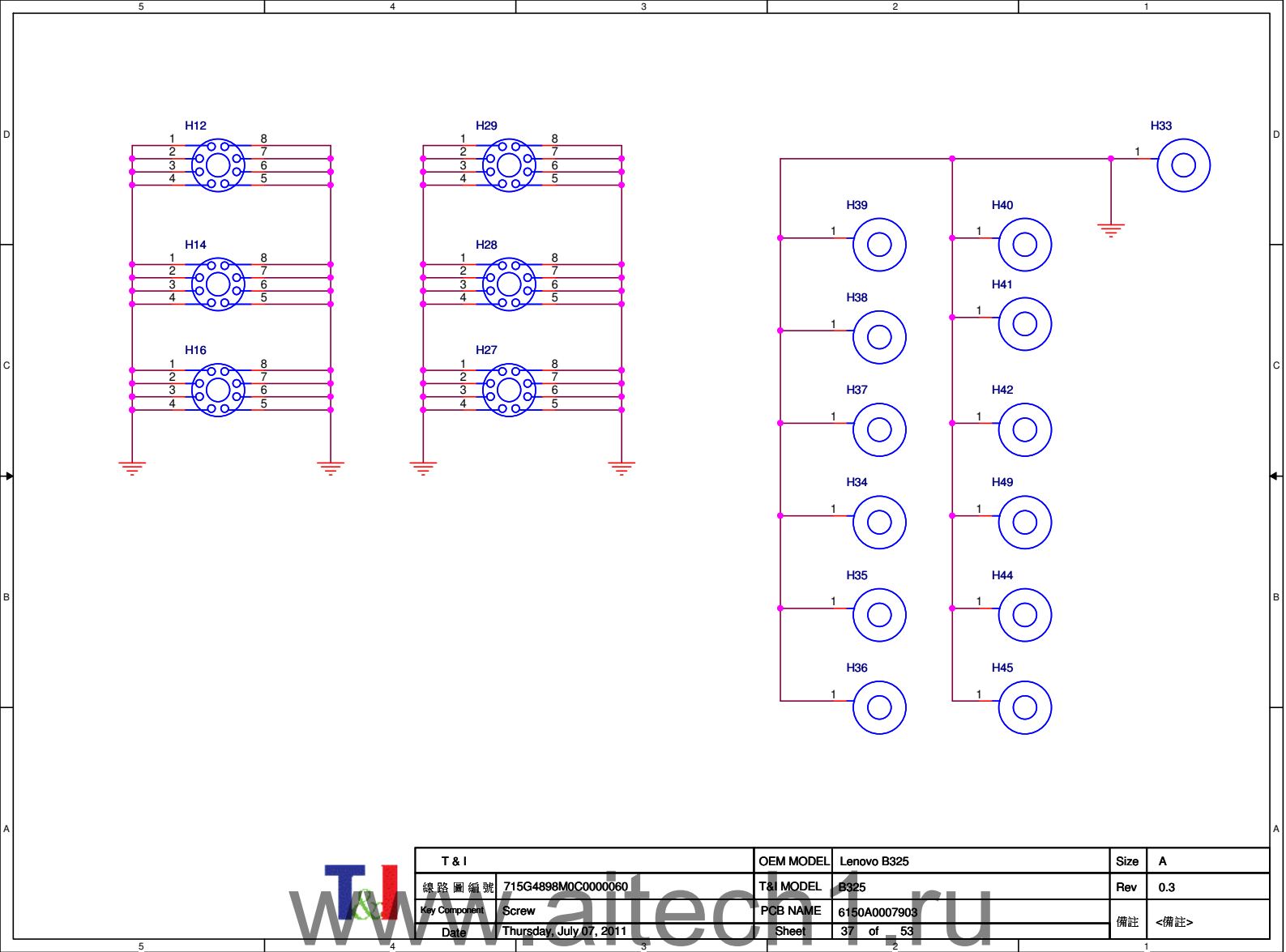


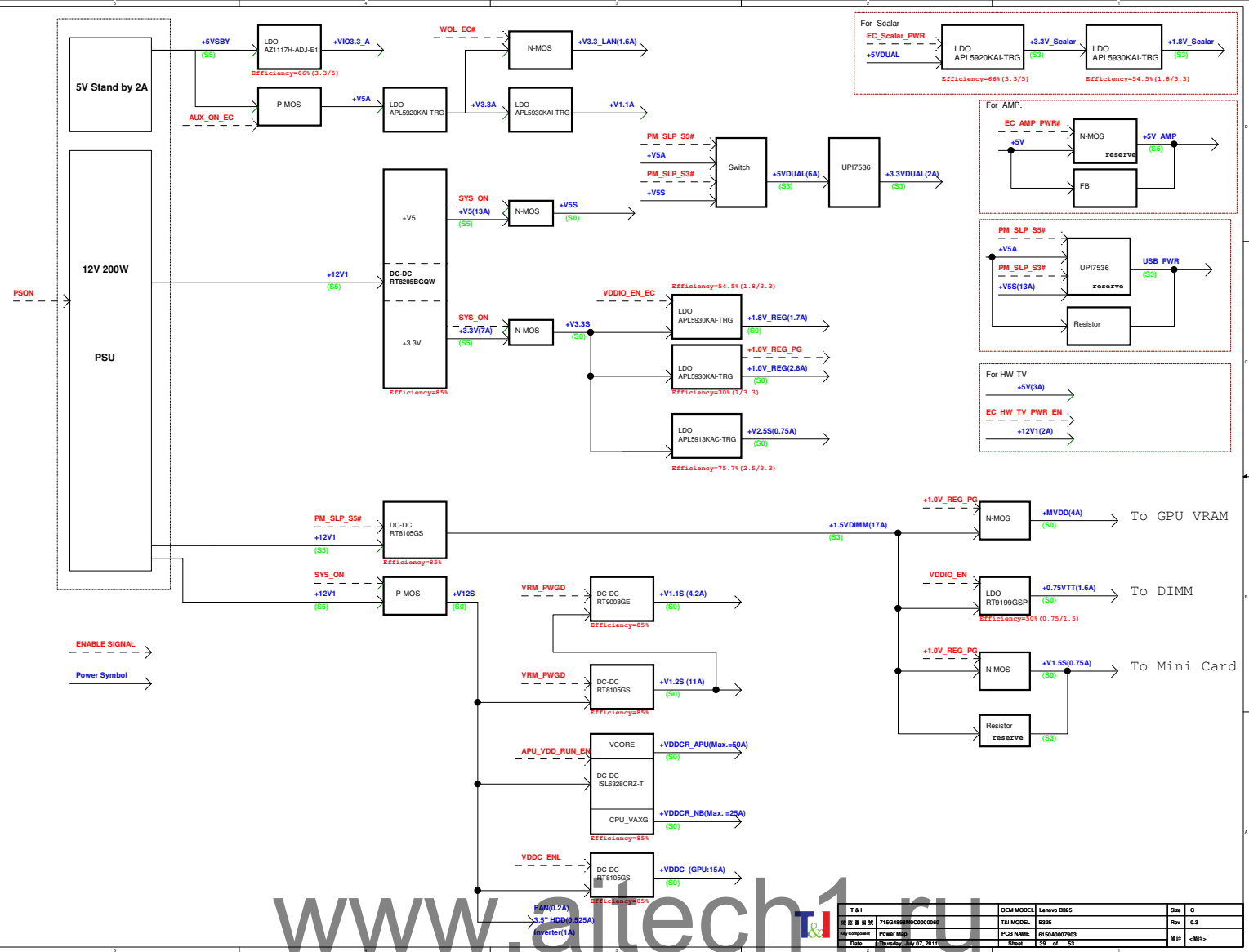
SOURCE# : L (PC MODE), H (HW/TV MODE)

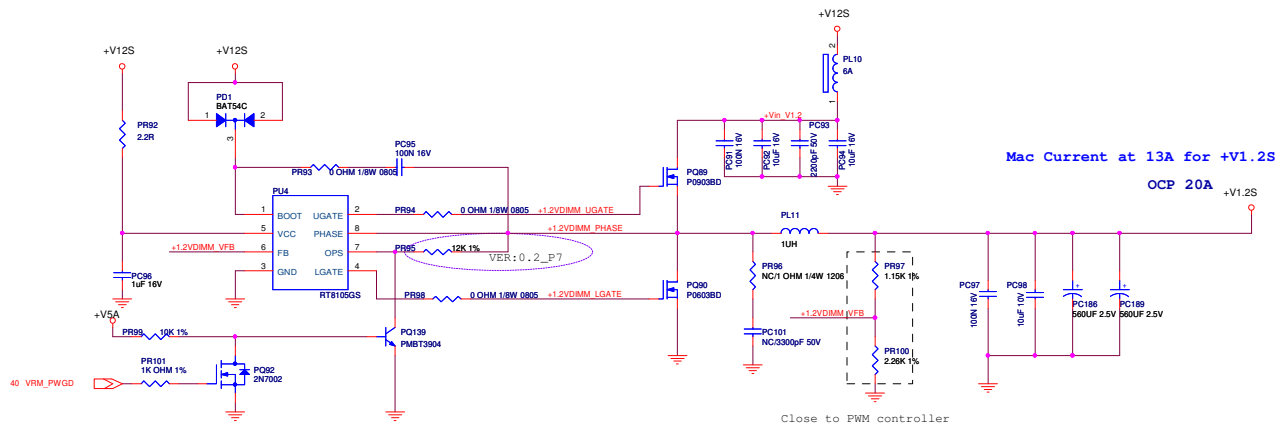


T & I		OEM MODEL	Lenovo B325	Size	B
微 鏡 置 備 號	715G4898MOC0000060	T&I MODEL	B325	Rev	0.3
Key Component	HW/TV_CONN	PCB NAME	6150A0007903	廠 註	<備註>
Date	Thursday, July 07, 2011	Sheet	36 of 53		

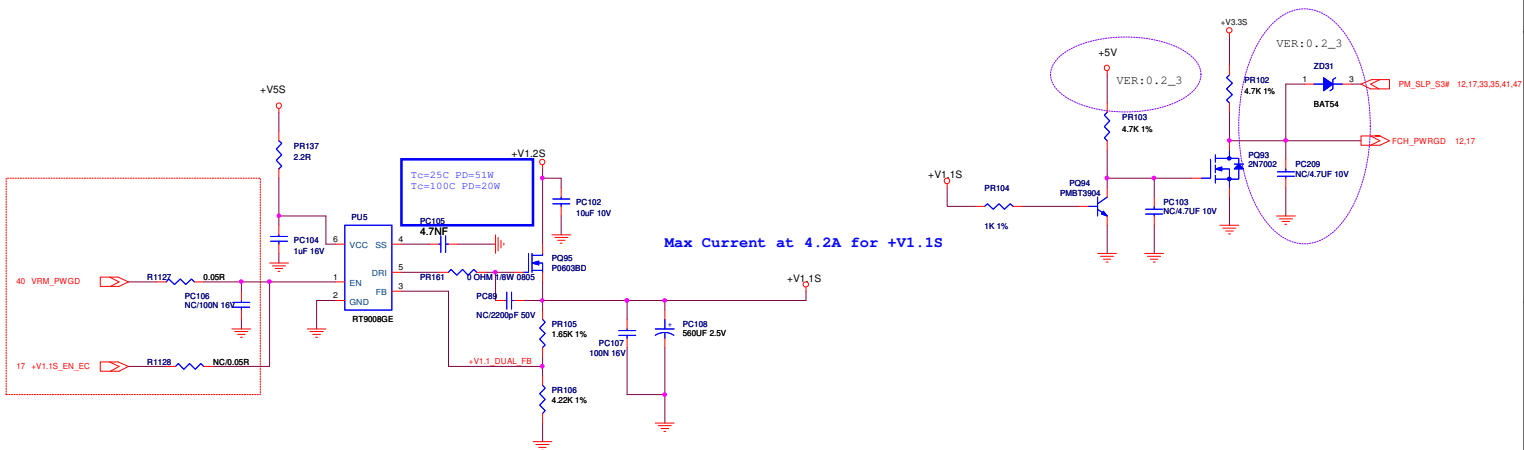
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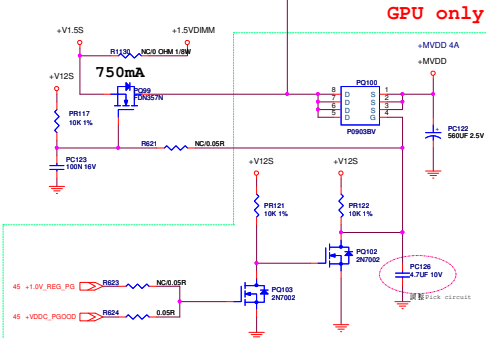
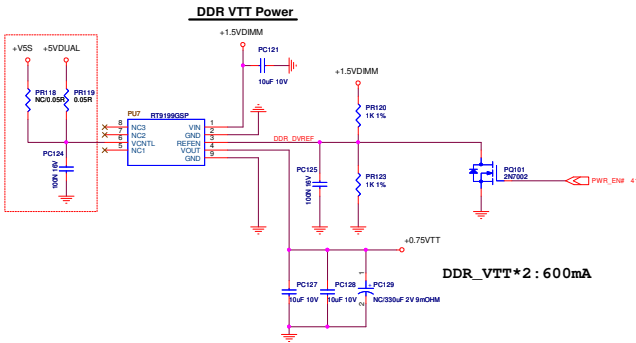
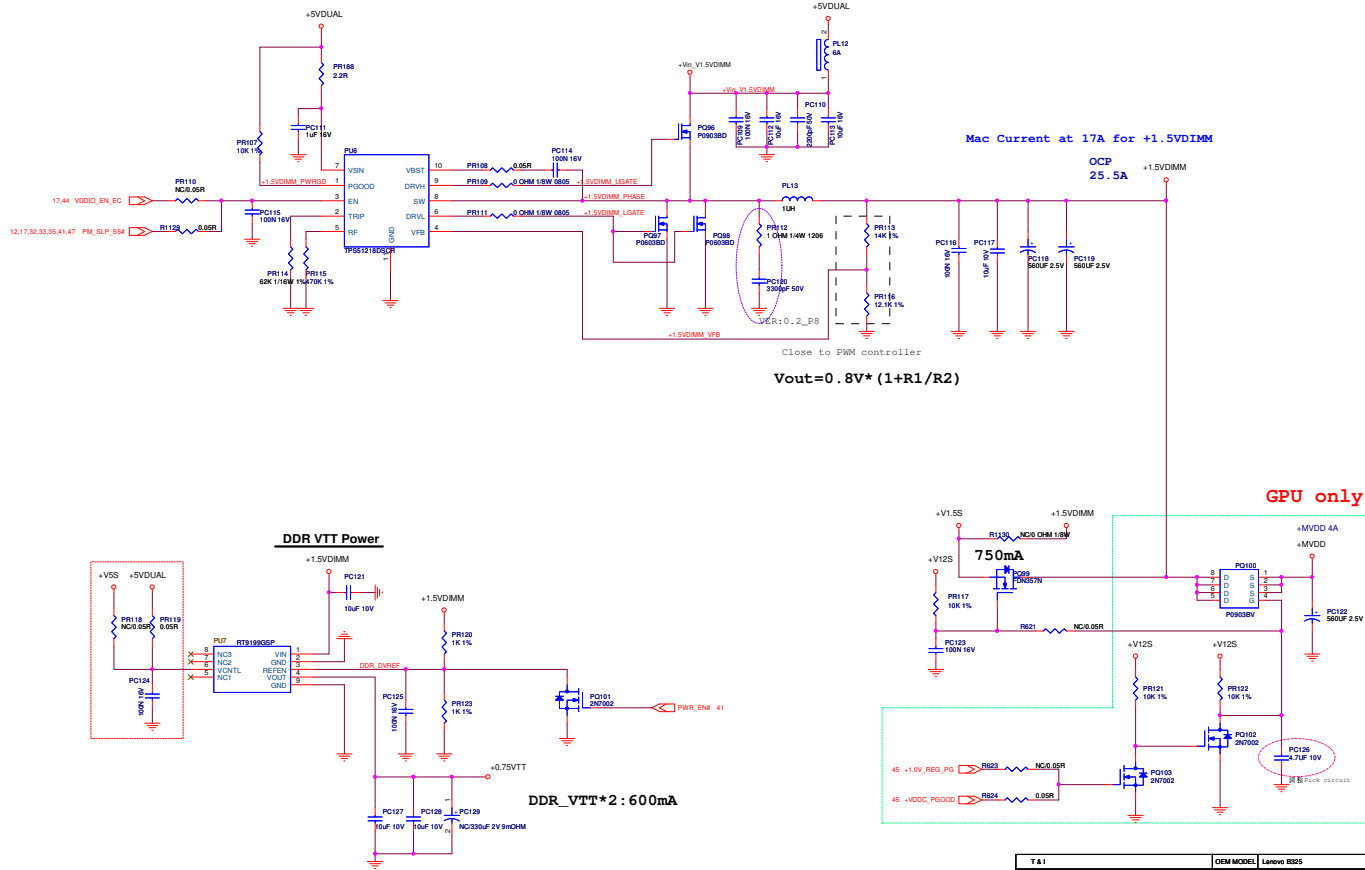


$$V_{out} = 0.8V * (1 + R1/R2)$$



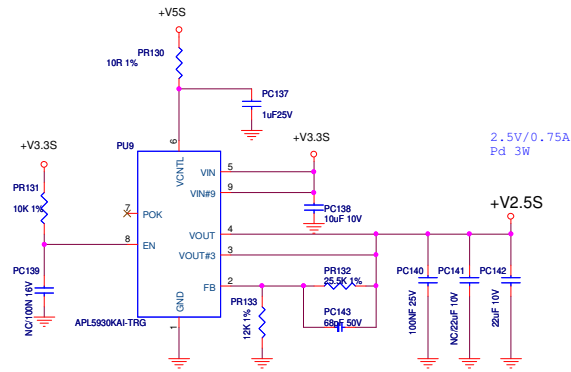
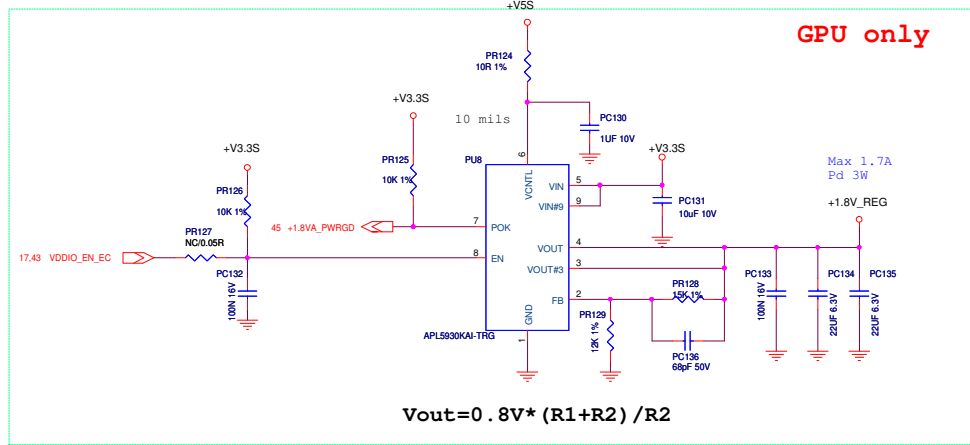
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線路圖編號	715G4888MOC0000060	T&I MODEL	B325	Rev	0.3
Key Component	+V1.1S n +V1.2S	PCB NAME	6150A0007903		
Date	Thursday, July 07, 2011	Sheet	42 of 53	備註	<備註>

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T&I	Q&M MODEL	Lenovo BIOS	Rev	Custom
71504888888888888888	TAI MODEL	8025	Rev	8.5
+1.5VDIMM +1.5VLT -0.75VTT	PCB NAME	81504888888888888888	Sheet	43 of 53
Date	Thursday, July 27, 2011			

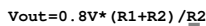
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T & I	OEM MODEL	Lenovo B325	Size	B
統 路 圖 編 號	T&I MODEL	B325	Rev	0.3
Key Component	PCB NAME	6150A0007903	備註	<備註>
Date	Thursday, July 07, 2011	Sheet	44	of 53

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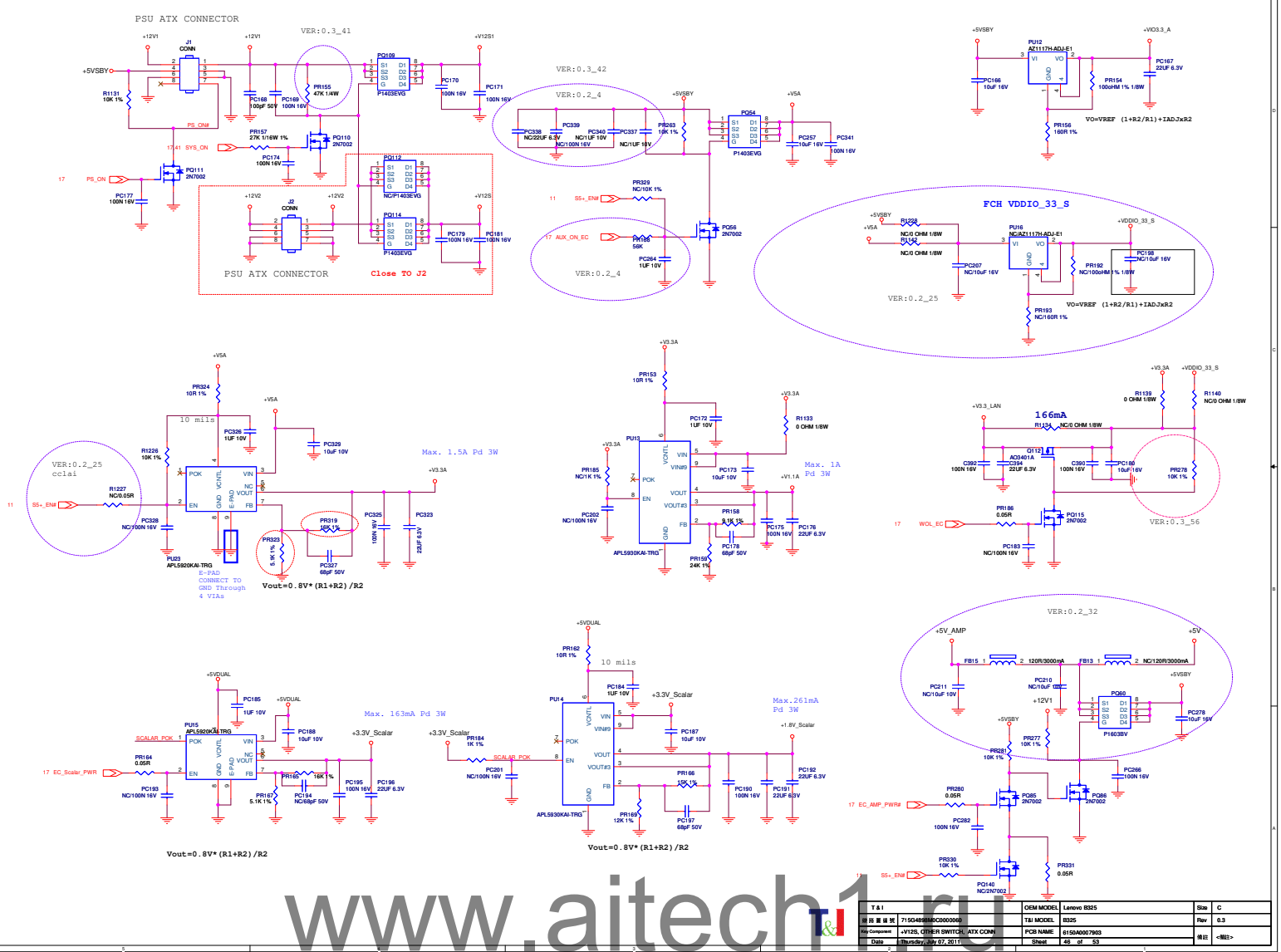
GPU only



PCNTL0	PCNTL1	GPU(計算値)
0	0	1.0V

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T & I		OEM MODEL		Lenovo B325	Size	C
零件編號	715G4898M0C0000060	TAI MODEL		B325	Rev	0.3
Key Component	+VDDC, +3.3V_REG	PCB NAME		8150A0007903		
Date	Thursday, July 07, 2011	Sheet		45 of 53	請注意 <腳位>	

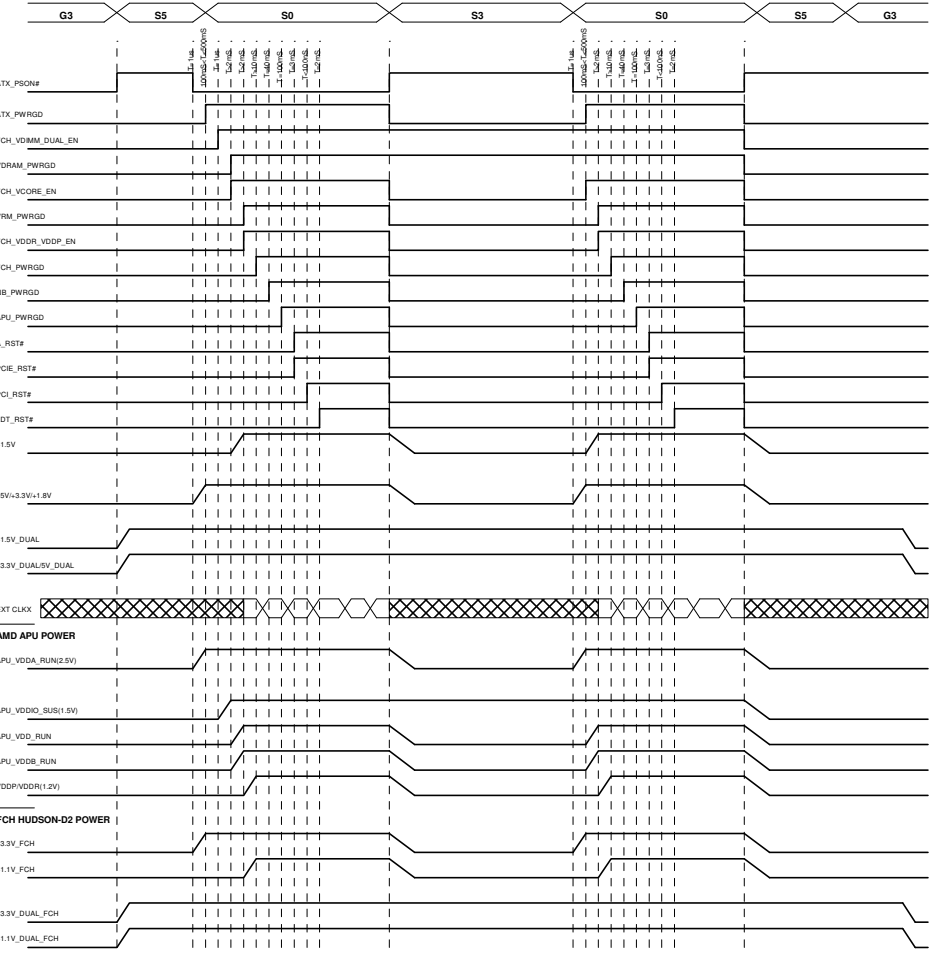


T & I		OEM MODEL	Lenovo B325	Size	C
牌路通英	715G489MDC000069	TAI MODEL	B325	Rev	0.3
Key Comment	+V125, OTHER SWITCH, ATX CONN		PCB NAME	6150AD007903	
Date	Thursday, July 07, 2011		Sheet	46 of 53	
				請往	<圖>

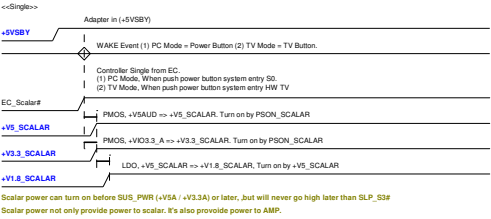


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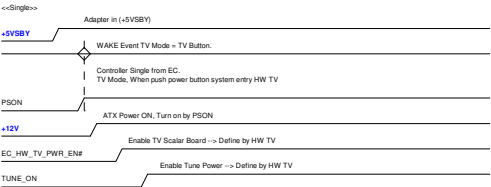
Power ON/OFF Sequence



Scalar Power



TV Power

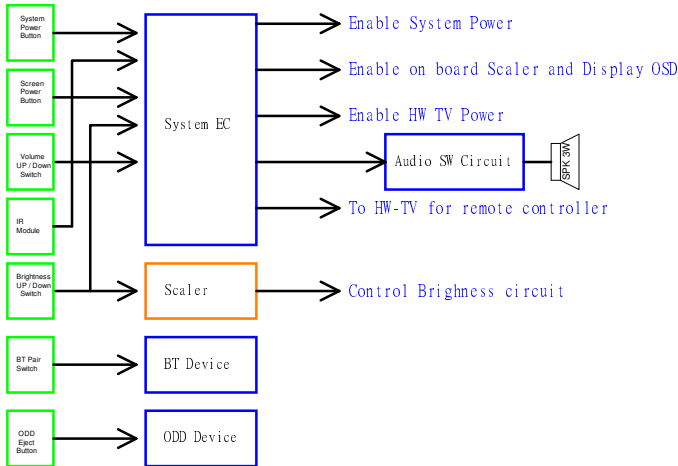


T&I	Q&M&C&S	Lenovo M&S	Rev	Custom
1.0	1.0	1.0	Rev	Rev
Rev	Rev	Rev	Rev	Rev
Date	Monday, 22/07/2011	Rev	Rev	Rev

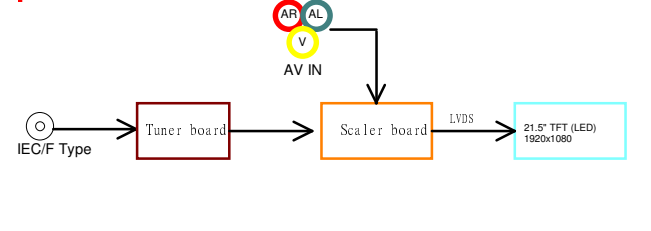
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Function Request.

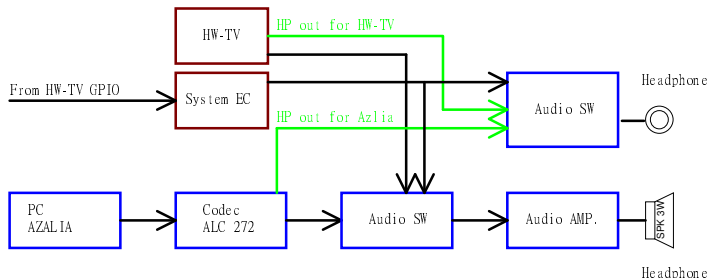
System Power Button	LCD/HW-TV Power Button	Brightness UP/Down Switch	Volume UP/Down Switch	BT Pair Switch	ODD Eject Button
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Optional for HW TV Function



Audio SW Circuit

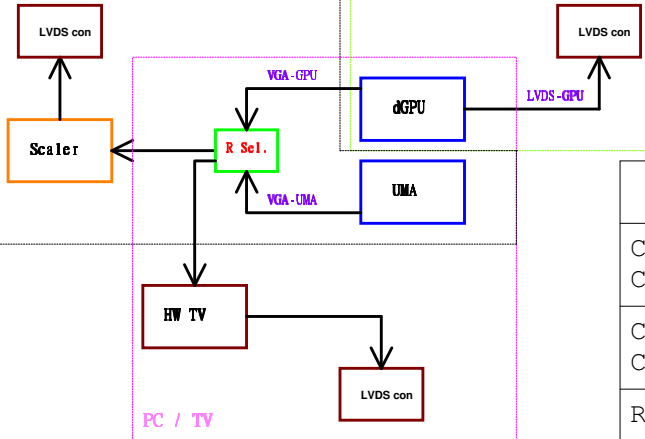


T & I		OEM MODEL	Lenovo B325	Size	Custom
機器重複號	715G4898MOC0000060	T&I MODEL	B325	Rev	0.3
Key Component	Block_Key and Audio Switch	PCB NAME	6150A0007903		
Date	Thursday, July 07, 2011	Sheet	49 of 53	備註	<備註>

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Only PC for UMA

Only PC for dGPU

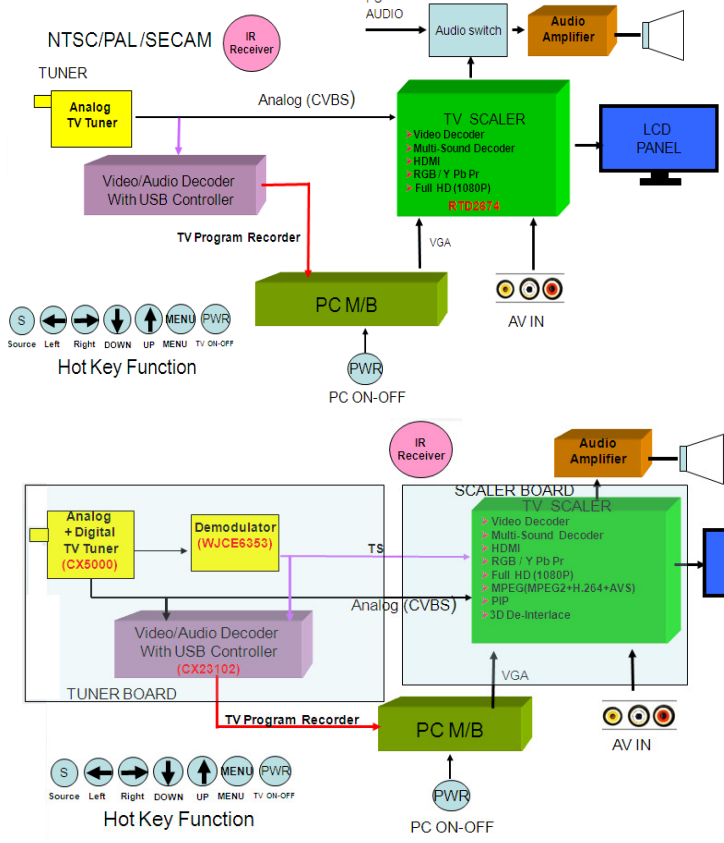


GPU	UMA
C271.C272.C273.C274-->0.1U C275.C279.C280.C285-->0.1U	C271.C272.C273.C274-->NC C275.C279.C280.C285-->NC
C449.C450.C454.C453-->NC C337.C339.C338.C340-->NC	C449.C450.C454.C453-->0.1U C337.C339.C338.C340-->0.1U
R808.R809-->0R	R808.R809-->NC
R836.R837-->NC	R836.R837-->0R
R427.R428-->0R	R427.R428-->NC
R429.R431-->NC	R429.R431-->0R
R416.R407.R400-->0R	R416.R407.R400-->NC
R419.R408.R401-->NC	R419.R408.R401-->0R
R424.R439-->0R	R424.R439-->NC
R422.R437-->NC	R422.R437-->0R



T & I		OEM MODEL	Lenovo B325	Size	B
統 路 圖 編 號	715G4898MOC0000060	T&I MODEL	B325	Rev	0.3
Key Component	Block_LVDS Display Diagram	PCB NAME	6150A0007903	備註 <備註>	
Date	Thursday, July 07, 2011	Sheet	50 of 53		

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Use HWTB	ONLY PC
JP4 -->7-8 short	JP4 -->7-8 NC
JP2 --> short	JP2 --> NC
P17. R797.R798-->NC	P17. R797.R798-->USE
P17. R1174.R1175-->USE	P17. R1174.R1175-->NC
P22. R426.R430-->NC	P22. R426.R430-->USE
P34. R727.R728-->NC	P34. R727.R728-->USE
P36. R750.R751-->USE	P36. R750.R751-->NC

	ID1	ID2	ID4
21" LG	HIGH	HIGH	HIGH
21" AUO	HIGH	LOW	HIGH
20" LG	LOW	HIGH	HIGH
20" CMI	LOW	LOW	HIGH
21" SA	HIGH	HIGH	LOW
20" SA	HIGH	LOW	LOW



T&I	OEM MODEL	Lenovo B325	Size	Custom
715G4888MOC000000	T&I MODEL	B325	Rev	0.3
HW/TV_Diagram	PCB NAME	6150A0007903	備註	<None>
Date	Thursday, July 07, 2011	Sheet	51 of 53	

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REVISION HISTORY					
Date	Ver	Description	Date	Ver	Description
03/02	0.2	1.P11.P15 : R1103.R1104.R1042.R1048-->NC ADD R1049.R1041-->10K;disable debug mode setting for boot	03/03	0.2	P1.P41 : PR67--C-->17.8K , PR69 --C-->11.5K ; Modify feedback to 5.1V output
03/02	0.2	2.P12 : R956-->NC R957-->0R ; change to EC output PWRBT signal	03/03	0.2	P2.P40 : PR17--C-->1.02k (061G04021021FI)
03/02	0.2	3.P42 : ADD ZD31 , PR 103--C-->PU +5V ; Let PWRGD signal match SPEC	03/03	0.2	P3.P41 : PR70--C-->64.9k (061G04026492FI)
03/02	0.2	4.P46 : ADD PC337,PC338,PC339,PC340,PC264,PR168 ; Let PQ54 turn on voltage no drop	03/03	0.2	P4.P41 : PR71--C-->150k ohm (061G04021503FI)
03/02	0.2	5.P17 : R1059.1060-->NC ; PU on rear IO board	03/03	0.2	P5.P41 : PR76, PC85, PR78, PC86 snubber on Board
03/02	0.2	6.P40 : PR8--C--> PU +V1.2S , PR4--C--> PU +3.3V ; Modify Vcore PWROK timing	03/03	0.2	P6.P41 : PC82 .PC83--C-->560U (067G 20556110B)
03/02	0.2	7.P12 : R992-->NC , ADD R1146 -->0R ; R992 had internal PU, 0514 Delete R1146 .	03/03	0.2	P7.P42 : PR95--C-->12k (061G04021202FI)
03/02	0.2	8.P11 : C1397.C1398.C1393--C-->12P , C1394--C-->15P ADD R1171-->0R ; X'tal vendor recommend	03/03	0.2	P8.P43 : PR112, PC120 snubber on Board
03/02	0.2	9.P5 : R875.R876.R1010--C-->1K ; Let UMA DVI and VGA display	03/03	0.2	P9.P45 : PR141--C--> 43k(061G04024302FI)
03/02	0.2	10.P5 : C1296.C1269,R890.R909-->Del , ADD R1172.1173-->0R , Q94.Q96,R1197.R1198-->NC ;	03/03	0.2	P10.P40 : PR25.PR33--C--> 820R (061G04028200FI)
03/02	0.2	11.P22 : R810.R811.R812.R813.R814.R820.R821.R822-->0R ; Cut APU DVI interface	03/03	0.2	P11.P40 : PR46--C--> 453R (061G04024530FF)
03/02	0.2	12.P30 : Q36--C-->2N7002; debug GPU reboot no display	03/03	0.2	P12.P40 : PR32--C--> 1.69k (061G04021691FI)
03/02	0.2	13.P22 : ADD R1202.R1201-->10K , ADD Q107-->2N7002 ; modify Panel ID setting	03/03	0.2	P13.P40 : PR59 --C--> 2.2R (061G0603229 JI)
03/02	0.2	14.P17 : ADD R1174.R1175-->0R ; Follow B320	03/03	0.2	P14.P40 :PR43, PR44, PC46, PR48, PC45, PC2, PC3, PU2, PR61, PC54,PR62, PQ74
03/02	0.2	15.P24 : R467.R469-->NC ; NC double PU			PR63, PC55, PC56, PC57, PC58, PL4, PQ77, PQ78, PC63-->NC
03/02	0.2	16.P35 : R410,Q85.Q86-->NC ; Follow B320	03/03	0.2	P15.P47 :PR170.PR191--C-->680R (061G1206681 JT)
03/02	0.2	17.P22 : ADD R855,R863-->0R , R1177-->10K; Follow B320	03/03	0.2	P16.P47 :PR171.PR172--C-->240R (061G0805241 JI)
03/03	0.2	18.P33 : C782--C--> ; Match LENOVO AVL	03/07	0.2	DEL ALL PSTP
03/03	0.2	19.P12 : ADD Q87-->2N7002 ; for TV update FW	03/10	0.2	P17.P40 :PR16--C-->12K (061G0402123 JI)
03/07	0.2	20.P17 : R1093--C-->PU GND , C1469--> NC ;	03/10	0.2	P18.P40 :PC20--C-->100pF (065G040210131J T)
03/07	0.2	21.P11 : ADD R1203.R1204.R1205.R1206.R1207-->22R ;	03/10	0.2	P19.P40 :PC24--C-->2.2nF (065G04022232K Y)
03/07	0.2	22.P46 : ADD R1209.R1210,PC208.PQ57,PQ58;	03/10	0.2	P20.P40 :PR27--C-->2K (061G04022001FI) , PR328-->49.9K (061G04024992FI)
03/09	0.2	23.P20 : ADD R386.R372,Q71.Q72 , R1199 -->NC ;			PR28--C-->11K (061G04021102FT) , PR29--C-->750R (061G04027500FI)
03/09	0.2	24.P18 : ADD C1502-->0.1u ;			PC33--C-->330pF (065G040233131J Y) , PC34--C-->220pF (065G040222131J T)
03/09	0.2	25.P46.P15.P12.P17:ADD PU16.PR192.PR193.PC198.PC207, ADD R1215.1216.R1217,R1053.R1214,R1046-->NC	03/10	0.2	P21.P40 :PR55--C-->12K (061G0402123 JI)
		,ADD Q111.R1220, changed P14 Power source ;ADD S5+ function	03/15	0.2	P22.P41 :PC78--C-->10uF (065G080510615K T)
03/10	0.2	26.P17 : PIN35--C-->PIN71 ;			
03/10	0.2	27.DEL PCIE USB3.0 interface			
03/10	0.2	28.P14 : ALL BEAD 220R--C-->120R			
03/12	0.2	29.P21 : NC -->R1219.611.1192.1193.1194,C1499,Q32.33.39.41;			
03/14	0.2	30.P41 : ADDC1296-->10U ; Let +V5A no drop when +5VDUAL switch from +V5S			
03/14	0.2	31.P12.P19 : R988--C-->56R,R357.R358--C-->47R ;			
03/14	0.2	32.P46 : FB13--C-->NC,ADD PQ60,PC210.PC211;			
03/14	0.2	33.P14 : ADD Q110 ; AMD errate			
03/14	0.2	34.P46 : ADD Q116 ,change WOL_EC-->HIGH active; for LAN power S3.S4.S5 wake up function			
03/14	0.2	35.P33.P35 : Modify USB3.0 signal connect to SATA connector			
			<div><div><div>T&I</div><div>緒形 重雄</div><div>Key Component</div><div>Date</div></div><div><div>715G4898MCC0000080</div><div>HW/TV Diagram</div><div>Thursday, July 07, 2011</div></div><div><div>OEM MODEL</div><div>T&I MODEL</div><div>PCB NAME</div><div>Sheet</div></div><div><div>Lenovo B325</div><div>B325</div><div>6150A0007903</div><div>S2 of S3</div></div><div><div>Size</div><div>Rev</div><div>備註</div><div>A3</div><div>0.3</div><div><備註></div></div></div>		

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REVISION HISTORY

Date	Ver	Description	Date	Ver	Description
03/15	0.2	36.P17 : ADD R1221,Del ZD19 ;	05/17	0.3	68.P34 :Add R1236, C1053 Reserve Mini Pci Pin41, 39 standby-by power
03/15	0.2	36.P19 : R834.R835--C-->51K,R872.R1035--C-->300K ;	05/17	0.3	69.P34 :Change R723, R718 Pull hi voltage.
03/15	0.2	37.P20 : C328.C325-->NC,C847.C326-->1UF ;	05/17	0.3	70.P34 :Add PERST# Depen Resister.
03/15	0.2	38.P22 :R838.839.840.842.843.1113.1115.1117--C-->NC;	05/17	0.3	71.P12 :Add ACF detect Pin.
03/15	0.2	39.P22 :ADD Q113.Q114,R1222.R1223; For GPIO write to SCALAR	05/17	0.3	72.P17 :Change ADD ZD19,Del R1212
03/16	0.2	40.P08.P09 :DIMM A1.B0 change address	05/17	0.3	73.P13 :Change SATA Rcomp Resister 1K to 909 ohm
			05/17	0.3	74.P12.13 :Del R968 & R1010 fix pick current
			05/17	0.3	75.P12 :Change R955 10K
			05/17	0.3	76.P11 :Change r1171 1K5
05/03	0.3	41.P46 :modify PR155 -->47Kohm	05/17	0.3	77.P11 :Change X2 32.768 CL=9PF
05/03	0.3	42.P46 :PC337,PC338,PC339,PC340-->NC	05/17	0.3	78.P12 :Change R955 10K
05/03	0.3	43.P41 :C1296-->NC			
05/03	0.3	44.P14 :FB52-->NC,add FB103			
05/03	0.3	45.P15 :R1053-->NC,add R1046	06/16	0.4	79.P36 :ADD Poly fuse EMI Issue
05/03	0.3	46.P12 :R1215,R1216,Q87-->NC	06/29		Change reference name = PCPWM1, LEDB1, TPAD1, TVREC1
05/03	0.3	47.P12 :R1123,R1132-->NC,add R1114,R1116			
05/03	0.3	48.P33 :USB1,USB2-->088G 352 12 SY HF			
05/03	0.3	49.P33 :USB3-->033G8S1B 7 U			
05/03	0.3	50.P33 :C782-->560uF			
05/03	0.3	51.P35 :Change T_PDA1 Pull hi voltage.			
05/03	0.3	52.P22 :Q107-->NC,add R1231			
05/03	0.3	53.P11 :D19-->NC,add R1212			
05/03	0.3	54.P11 :add CN19			
05/03	0.3	55.P17 :Change EC Pin 112 net name WWOL			
05/03	0.3	56.P46 :Add PR278 Pull hi resistor to Power.			
05/03	0.3	57.P41 :Add C856 to Q78 Gate Pin Pull Down cap to GND.			
05/03	0.3	58.P41 :Add PR334 change EN control			
05/17	0.3	59.P41 :Add PC314 pull down cap to GND.			
05/17	0.3	60.P13 :Add SPI_CLK depen Resister .WP# Pull Low resister.			
05/17	0.3	61.P17 :Change R799 & R795 Pull hi +VIO3.3A.			
05/17	0.3	62.P17 :Change R1106 & R1107 Pull hi +VIO3.3A.			
05/17	0.3	63.P22 :Change R1222 & R1223 Pull hi +VDDP			
05/17	0.3	64.P22 :Change R418 & R420 Pull hi +VDDP			
05/17	0.3	65.P22 :Add level shift circuit.			
05/17	0.3	66.P05 :Change R1143 & R1145 Pull hi voltage.			
05/17	0.3	67.P34 :Add R1234, R1235 Select mini Pci In-put voltage.			



T & E		OEM MODEL	Lenovo B325	Size	A3
線路圖編號		T&E MODEL	B325	Rev	0.3
Key Component		HW/TV Diagram	PCB NAME	6150A0M07903	
Date		Thursday, July 07, 2011	Sheet	53	of 53
				備註	<備註>

REVISION HISTORY

[illegible]

T & I		OEM MODEL	Lenovo B325	Size	A3
機殼號碼	715G4898MOC0000060	T&I MODEL	B325	Rev	0.3
Key Component	HW/TV_Diagram	PCB NAME	6150A0007903	備註 <備註>	
Date	Thursday, July 07, 2011	Sheet	53 of 53		

				Key Component	HW/TV Diagram	PCB NAME
				Date	Thursday, July 07, 2011	Sheet

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